

# Types for Deadlock-freedom: The Synchronous Case

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## Abstract

Many different notions of “program property”, and many different methods of verifying such properties, arise naturally in programming. We present a general framework of *Specification Structures* for combining different notions and methods in a coherent fashion. We then apply the idea of specification structures to concurrency in the setting of Interaction Categories. As a specific example, a certain specification structure defined over the interaction category  $\mathcal{S}Proc$  yields a new category  $\mathcal{S}Proc_D$  whose type system is strong enough to guarantee deadlock-freedom of concurrent processes. We present some techniques for manipulating typed processes in this category, and show that they allow us to reason about deadlock-freedom in *synchronous networks*, a class of concurrent systems which incorporates both synchronous dataflow programs and systolic algorithms.

# 1 Introduction

*Type Inference* and *Verification* are two main paradigms for constraining the behaviour of programs in such a way as to guarantee desirable properties. Although they are generally perceived as distinct, on closer inspection it is hard to make any very definite demarcation between them; type inference rules shade into compositional proof rules for a program logic. Indeed, type inference systems, even for the basic case of functional programming languages, span a broad spectrum in terms of expressive power. Thus, ML-style types [39] are relatively weak as regards expressing behavioural constraints, but correspondingly tractable as regards efficient algorithms for “type checking”. System F types [24] are considerably more expressive of polymorphic behaviour, and System F typing guarantees Strong Normalization. However, System F cannot express the fact that a program of type  $\text{list}[\text{nat}] \Rightarrow \text{list}[\text{nat}]$  is actually a sorting function. Martin-Löf type theory [41], with dependent types and equality types, can express complete total correctness specifications. In the richer theories, type checking is undecidable [47].

One might try to make a methodological distinction: post-hoc verification vs. constructions with intrinsic properties. However, this is more a distinction between ways in which Type Inference/Verification can be deployed than between these two formal paradigms.

We suggest that it is the rule rather than the exception that there are many different notions of “properties of interest” for a given computational setting. Some examples follow.

- Even in the most basic form of sequential programming, it has proved fruitful to separate out the aspects of partial correctness and termination, and to use different methods for these two aspects [19].
- In the field of static analysis, and particularly in the systematic framework of abstract interpretation [28], a basic ingredient of the methodology is to use a range of non-standard interpretations to gain information about different properties of interest.
- In concurrency, it is standard to separate out classes of properties such as safety, liveness, and fairness constraints, extending into a whole temporal hierarchy, and to apply different methods for these classes [34].

The upshot of this observation is that no one monolithic type system will serve all our purposes. Moreover, we need a coherent framework for moving around this space of different classes of properties.

The basic picture we offer to structure this space is the “tower of categories”:

$$\mathcal{C}_0 \leftarrow \mathcal{C}_1 \leftarrow \mathcal{C}_2 \leftarrow \cdots \leftarrow \mathcal{C}_k.$$

The idea behind the picture is that we have a semantic universe (category with structure)  $\mathcal{C}_0$ , suitable for modelling some computational situation, but possibly carrying only a very rudimentary notion of “type” or “behavioural specification”. The tower arises by refining  $\mathcal{C}_0$  with richer kinds of property, so that we obtain progressively richer settings for performing specification and verification. Of course, non-linear patterns of refinement—trees or dags rather than sequences—can also be considered, but the tower suffices to establish the main ideas.

The remainder of this paper provides a detailed development of this idea in the setting of interaction categories [1, 5, 6], with particular reference to synchronous systems. Section 2 introduces the notion of a *specification structure*, which formalizes the idea of enriching a

semantic universe with a refined notion of property. Section 3 reviews the theory of interaction categories and defines  $\mathcal{S}Proc$ , a category of synchronous processes. In Section 4 we explicitly state the requirements for a specification structure to be defined over an interaction category such as  $\mathcal{S}Proc$ , and in Section 5 we define a particular specification structure over  $\mathcal{S}Proc$ . The result is a category  $\mathcal{S}Proc_D$ , in which types are strong enough to specify deadlock-freedom, and deadlock-freedom can be verified compositionally. In Section 6 we give an alternative presentation of this specification structure, which is sometimes more convenient for calculations, and prove that it is equivalent. As an application of the theory developed in the rest of the paper, Section 7 analyses the construction of a class of *synchronous networks*, which encompasses both synchronous dataflow programs in languages such as SIGNAL [25] and LUSTRE [26], and systolic algorithms [20]. Finally we compare our theory with other approaches, and discuss the possibilities for further developments.

## 2 Specification Structures

The notion of specification structure, at least in its most basic form, is quite anodyne, and indeed no more than a variation on standard notions from category theory. Nevertheless, it provides an alternative view of these standard notions which is highly suggestive, particularly from a Computer Science point of view. Similar notions have been studied, for a variety of purposes, by Burstall and McKinna [35], O’Hearn and Tennent [42], and Pitts [44].

**Definition 2.1** *Let  $\mathcal{C}$  be a category. A specification structure  $S$  over  $\mathcal{C}$  is defined by the following data:*

- for each object  $A$  of  $\mathcal{C}$ , a set  $P_S A$  of “properties over  $A$ ”.
- for each pair of objects  $A, B$  of  $\mathcal{C}$ , a relation  $S_{A,B} \subseteq P_S A \times \mathcal{C}(A, B) \times P_S B$ .

We write  $\varphi\{f\}\psi$  for  $S_{A,B}(\varphi, f, \psi)$  (“Hoare triples”). This relation is required to satisfy the following axioms, for  $f : A \rightarrow B$ ,  $g : B \rightarrow C$ ,  $\varphi \in P_S A$ ,  $\psi \in P_S B$  and  $\theta \in P_S C$ :

$$\varphi\{\text{id}_A\}\varphi \tag{1}$$

$$\varphi\{f\}\psi, \psi\{g\}\theta \implies \varphi\{f ; g\}\theta \tag{2}$$

The axioms (1) and (2) are typed versions of the standard Hoare logic axioms for “skip” and “sequential composition” [19]. Given  $\mathcal{C}$  and  $S$  as above, we can define a new category  $\mathcal{C}_S$ . An object of  $\mathcal{C}_S$  is a pair  $(A, \varphi)$  with  $A \in \text{ob } \mathcal{C}$  and  $\varphi \in P_S A$ . A  $\mathcal{C}_S$ -morphism  $f : (A, \varphi) \rightarrow (B, \psi)$  is a morphism  $f : A \rightarrow B$  in  $\mathcal{C}$  such that  $\varphi\{f\}\psi$ .

Composition and identities are inherited from  $\mathcal{C}$ ; the axioms (1) and (2) ensure that  $\mathcal{C}_S$  is a category. Moreover, there is a faithful functor

$$\mathcal{C} \hookrightarrow \mathcal{C}_S$$

given by

$$A \mapsto (A, \varphi).$$

In fact, the notion of “specification structure on  $\mathcal{C}$ ” is coextensive with that of “faithful functor into  $\mathcal{C}$ ”. Given such a functor  $F : \mathcal{D} \rightarrow \mathcal{C}$ , we can define a specification structure  $S$  by:

$$\begin{aligned} P_S A &= \{\varphi \in \text{ob } \mathcal{D} \mid F(\varphi) = A\} \\ \varphi\{f\}\psi &\iff \exists \alpha \in \mathcal{D}(\varphi, \psi). F(\alpha) = f \end{aligned}$$

(by faithfulness,  $\alpha$  is unique if it exists). It is easily seen that this passage from faithful functors to specification structures is (up to equivalence) inverse to that from  $S$  to  $\mathcal{C} \leftarrow \mathcal{C}_S$ .

A more revealing connection with standard notions is yielded by the observation that specification structures on  $\mathcal{C}$  correspond exactly to lax functors from  $\mathcal{C}$  to  $\mathcal{Rel}$ , the category of sets and relations. Given a specification structure  $S$  on  $\mathcal{C}$ , the object part of the corresponding functor  $R : \mathcal{C} \rightarrow \mathcal{Rel}$  is given by  $P_S$ , while for the arrow part we define

$$R(f) = \{(\varphi, \psi) \mid \varphi\{f\}\psi\}.$$

Then (1) and (2) become precisely the statement that  $R$  is a lax functor with respect to the usual order-enrichment of  $\mathcal{Rel}$  by inclusion of relations:

$$\begin{aligned} \text{id}_{R(A)} &\subseteq R(\text{id}_A) \\ R(f); R(g) &\subseteq R(f; g). \end{aligned}$$

Moreover, the functor  $\mathcal{C} \leftarrow \mathcal{C}_S$  is the lax fibration arising from the Grothendieck construction applied to  $R$ .

The notion of specification structure acquires more substance when there is additional structure on  $\mathcal{C}$  which should be lifted to  $\mathcal{C}_S$ . Suppose for example that  $\mathcal{C}$  is a monoidal category, i.e. there is a bifunctor  $\otimes : \mathcal{C}^2 \rightarrow \mathcal{C}$ , an object  $I$ , and natural isomorphisms

$$\begin{aligned} \text{assoc}_{A,B,C} : (A \otimes B) \otimes C &\cong A \otimes (B \otimes C) \\ \text{unitl}_A : I \otimes A &\cong A \\ \text{unitr}_A : A \otimes I &\cong A \end{aligned}$$

satisfying the standard coherence equations [33]. A specification structure for  $\mathcal{C}$  must then correspondingly be extended with an action

$$\otimes_{A,B} : PA \times PB \rightarrow P(A \otimes B)$$

and an element  $I_S \in PI$  satisfying, for  $f : A \rightarrow B$ ,  $f' : A' \rightarrow B'$  and properties  $\varphi, \varphi', \psi, \psi', \theta$  over suitable objects:

$$\begin{aligned} \varphi\{f\}\psi, \varphi'\{f'\}\psi' &\implies (\varphi \otimes \varphi')\{f \otimes f'\}(\psi \otimes \psi') \\ ((\varphi \otimes \psi) \otimes \theta)\{\text{assoc}_{A,B,C}\} &(\varphi \otimes (\psi \otimes \theta)) \\ (I_S \otimes \varphi)\{\text{unitl}_A\} &\varphi \\ (\varphi \otimes I_S)\{\text{unitr}_A\} &\varphi. \end{aligned}$$

Such an action extends the corresponding lax functor  $R : \mathcal{C} \rightarrow \mathcal{Rel}$  to a lax monoidal functor to  $\mathcal{Rel}$  equipped with its standard monoidal structure based on the cartesian product.

Now assume that  $\mathcal{C}$  is symmetric monoidal closed, with natural isomorphism  $\text{symm}_{A,B} : A \otimes B \cong B \otimes A$ , and internal hom  $\multimap$  given by the adjunction

$$\mathcal{C}(A \otimes B, C) \cong \mathcal{C}(A, B \multimap C).$$

Writing  $\Lambda(f) : A \rightarrow B \multimap C$  for the morphism corresponding to  $f : A \otimes B \rightarrow C$  under the adjunction, we require an action

$$\multimap_{A,B} : PA \times PB \rightarrow P(A \multimap B)$$

and axioms

$$\begin{aligned} (\varphi \otimes \psi)\{\text{symm}_{A,B}\} &(\psi \otimes \varphi) \\ ((\varphi \multimap \psi) \otimes \varphi)\{\text{eval}_{A,B}\} &\psi \end{aligned}$$

$$(\varphi \otimes \psi)\{f\}\theta \implies \varphi\{\Lambda(f)\}(\psi \multimap \varphi).$$

Going one step further, suppose that  $\mathcal{C}$  is a  $*$ -autonomous category, i.e. a model for the multiplicative fragment of classical linear logic [13], with linear negation  $(\Leftrightarrow)^\perp$ , where for simplicity we assume that  $A^{\perp\perp} = A$ . Then we require an action

$$(\Leftrightarrow)_A^\perp : PA \rightarrow PA^\perp$$

satisfying

$$\begin{aligned} \varphi^{\perp\perp} &= \varphi \\ \varphi \multimap \psi &= (\varphi \otimes \psi^\perp)^\perp. \end{aligned}$$

Under these circumstances all of this structure on  $\mathcal{C}$  lifts to  $\mathcal{C}_S$ . For example, we define

$$\begin{aligned} (A, \varphi) \otimes (B, \psi) &= (A \otimes B, \varphi \otimes_{A,B} \psi) \\ (A, \varphi)^\perp &= (A^\perp, \varphi_A^\perp) \\ (A, \varphi) \multimap (B, \psi) &= (A \multimap B, \varphi \multimap_{A,B} \psi). \end{aligned}$$

All the constructions on morphisms in  $\mathcal{C}_S$  work exactly as they do in  $\mathcal{C}$ , the above axioms guaranteeing that these constructions are well-defined in  $\mathcal{C}_S$ . For example, if  $f : (A, \varphi) \rightarrow (B, \psi)$  and  $g : (A', \varphi') \rightarrow (B', \psi')$ , then

$$f \otimes g : (A \otimes A', \varphi \otimes \varphi') \rightarrow (B \otimes B', \psi \otimes \psi').$$

Moreover, all of this structure is preserved by the faithful functor  $\mathcal{C} \leftarrow \mathcal{C}_S$ .

The above example of structure on  $\mathcal{C}$  is illustrative. Exactly similar definitions can be given for a range of structures, including:

- models of classical (or intuitionistic) linear logic including the additives and exponentials [11]
- cartesian closed categories [18]
- models of polymorphism [18].

## 2.1 Examples of Specification Structures

In each case we specify the category  $\mathcal{C}$ , the assignment of properties  $P$  to objects and the Hoare triple relation.

1.  $\mathcal{C} = \mathit{Set}$ ,  $P_S X = X$ ,  $a\{f\}b \stackrel{\text{def}}{\Leftrightarrow} f(a) = b$ .

In this case,  $\mathcal{C}_S$  is the category of pointed sets.

2.  $\mathcal{C} = \mathit{Rel}$ ,  $P_S X = \{*\}$ ,  $*\{R\}*\stackrel{\text{def}}{\Leftrightarrow} \forall x \in A, y, z \in B. xRy \wedge xRz \Rightarrow y = z$ .

Then  $\mathcal{C}_S$  is the category of sets and partial functions.

3.  $\mathcal{C} = \mathit{Rel}$ ,  $PX = \varnothing X$ ,  $S\{R\}T \stackrel{\text{def}}{\Leftrightarrow} \forall x \in S. \{y \mid xRy\} \subseteq T$ .

This is essentially a typed version of dynamic logic [32], with the ‘‘Hoare triple relation’’ specialized to its original setting. If we take

$$\begin{aligned} S \otimes_{X,Y} T &= S \times T \\ S_X^\perp &= X \setminus S \end{aligned}$$

then  $\mathcal{C}_S$  becomes a model of classical linear logic.

4.  $\mathcal{C} = \mathcal{R}el, P_S X = \{C \subseteq X^2 \mid C = C^\circ, C \cap \text{id}_X = \emptyset\}$

$$\begin{aligned} C\{R\}D &\stackrel{\text{def}}{\iff} xCx', xRy, x'Ry' \Rightarrow yDy'. \\ C \otimes D &= \{((x, x'), (y, y')) \mid xCy \wedge x'Dy'\} \\ C \perp_X &= X^2 \setminus (C \cup \text{id}_X). \end{aligned}$$

$\mathcal{C}_S$  is the category of coherence spaces and linear maps [23].

5.  $\mathcal{C} = \mathcal{S}et, P_S X = \{s : \omega \rightarrow X \mid \forall x \in X. \exists n \in \omega. s(n) = x\},$

$$s\{f\}t \stackrel{\text{def}}{\iff} \exists n \in \omega. f \circ s \simeq t \circ \varphi_n$$

where  $\varphi_n$  is the  $n$ th partial recursive function in some acceptable numbering [45]. Then  $\mathcal{C}_S$  is the category of modest sets, seen as a full subcategory of  $\omega\text{-Set}$  [11].

6.  $\mathcal{C} =$  the category of SFP domains,

$P_S D = K\Omega(D)$  (the compact-open subsets of  $D$ ),

$$U\{f\}V \stackrel{\text{def}}{\iff} U \subseteq f^{-1}(V).$$

This yields (part of) Domain Theory in Logical Form [3], the other part arising from the local lattice-theoretic structure of the sets  $P_S D$  and its interaction with the global type structure.

7.  $\mathcal{C} =$  games and partial strategies, as in [10],  $PA =$  all sets of infinite plays,  $U\{\sigma\}V$  iff  $\sigma$  is *winning* with respect to  $U, V$  in the sense of [8]. Then  $\mathcal{C}_S$  is the category of games and winning strategies of [8].

These examples show the scope and versatility of these notions. Let us return to our picture of the tower of categories:

$$\mathcal{C}_0 \leftarrow \mathcal{C}_1 \leftarrow \mathcal{C}_2 \leftarrow \cdots \leftarrow \mathcal{C}_k.$$

Such a tower arises by progressively refining  $\mathcal{C}_0$  by specification structures  $S_1, \dots, S_k$  so that

$$\mathcal{C}_{i+1} = (\mathcal{C}_i)_{S_{i+1}}.$$

Each such step adds propositional information to the underlying “raw” computational entities (morphisms of  $\mathcal{C}_0$ ). The aim of verification in this framework is to “promote” a morphism from  $\mathcal{C}_i$  to  $\mathcal{C}_j$ ,  $i < j$ . That is, to promote a  $\mathcal{C}_0$  morphism  $f : A \rightarrow B$  to a  $\mathcal{C}_k$  morphism

$$f : (A, \varphi_1, \dots, \varphi_k) \rightarrow (B, \psi_1, \dots, \psi_k)$$

is precisely to establish the “verification conditions”

$$\bigwedge_{i=1}^k \varphi_i\{f\}\psi_i.$$

Once this has been done, by whatever means—model checking, theorem proving, manual verification, etc.—the morphism is available in  $\mathcal{C}_k$  to participate in typing judgements. In this way, a coherent framework for combining methods, including both compositional and non-compositional approaches, begins to open up. In the remainder of the paper, we will use specification structures defined over interaction categories to construct type systems for the specification and verification of concurrent systems.

### 3 The Interaction Category $\mathcal{S}Proc$

The theory of *Interaction Categories* has been proposed as a new paradigm for the semantics of sequential and concurrent computation [1, 5, 6]. The term encompasses certain known categories (the category of concrete data structures and sequential algorithms [14], categories of games [8], geometry of interaction categories [9]) as well as several new categories for concurrency. The fundamental examples of concurrent interaction categories are  $\mathcal{S}Proc$  [5], the category of synchronous processes, and  $\mathcal{AS}Proc$  [6], the category of asynchronous processes. The category  $\mathcal{S}Proc$  will be defined in this section; later we will use a specification structure over  $\mathcal{S}Proc$  to construct another interaction category.

The general picture of interaction categories is that the objects are types, which we also think of as specifications; the morphisms are concurrent processes which satisfy these specifications; and composition is interaction, i.e. an ongoing sequence of communications. The dynamic nature of composition in interaction categories is one of the key features, and is in sharp contrast to the functional composition typically found in categories of mathematical structures.

There is not yet a definitive axiomatisation of interaction categories, although some possibilities have been considered [21]. The common features of the existing examples are that they have  $*$ -autonomous structure, which corresponds to the multiplicative fragment of classical linear logic [23]; products and coproducts, corresponding to the additives of linear logic, and additional temporal structure which enables the dynamics of process evolution to be described.

#### 3.1 The Interaction Category $\mathcal{S}Proc$

In this section we briefly review the definition of  $\mathcal{S}Proc$ , the category of synchronous processes. Because the present paper mainly concerns the use of specification structures for deadlock-freedom, we omit the features of  $\mathcal{S}Proc$  which will not be needed in later sections. More complete definitions can be found elsewhere [1, 21].

An object of  $\mathcal{S}Proc$  is a pair  $A = (\Sigma_A, S_A)$  in which  $\Sigma_A$  is an *alphabet (sort) of actions (labels)* and  $S_A \subseteq^{nepref} \Sigma_A^*$  is a *safety specification*, i.e. a non-empty prefix-closed subset of  $\Sigma_A^*$ . If  $A$  is an object of  $\mathcal{S}Proc$ , a *process of type  $A$*  is a process  $P$  with sort  $\Sigma_A$  such that  $\text{traces}(P) \subseteq S_A$ . Our notion of process is *labelled transition system*, with *strong bisimulation* as the equivalence [36]. It is convenient to work with *synchronization trees* as canonical representatives of strong bisimulation classes. We will write  $\text{ST}(\Sigma)$  for the set of synchronization trees over a label-set  $\Sigma$ . We will usually define processes by means of labelled transition rules. The process of type  $A$  with no transitions is called  $\text{nil}_A$ , or sometimes  $\text{nil}$  if the type is clear from the context. We use the notation  $P \xrightarrow{a} P'$  for labelled transitions, and  $P \xrightarrow{s}^* P'$  when a trace (sequence of actions)  $s$  is involved. The empty trace is denoted  $\varepsilon$ , and concatenation of traces  $s$  and  $t$  is simply written  $st$ . We do not distinguish notationally between the action  $a$  and the trace containing only the action  $a$ .

If  $P$  is a labelled transition system,  $\text{traces}(P)$  is the set of sequences labelling finite paths from the root. The set of sequences labelling finite and infinite paths is  $\text{alltraces}(P)$  and the set of sequences labelling infinite paths is  $\text{inftraces}(P)$ . Equivalently, we may use the following coinductive definition.

$$\begin{aligned} \text{alltraces}(P) &\stackrel{\text{def}}{=} \{\varepsilon\} \cup \{a\sigma \mid P \xrightarrow{a} Q, \sigma \in \text{alltraces}(Q)\} \\ \text{traces}(P) &\stackrel{\text{def}}{=} \{\sigma \in \text{alltraces}(P) \mid \sigma \text{ is finite}\} \end{aligned}$$

$$\text{inftraces}(P) \stackrel{\text{def}}{=} \{\sigma \in \text{alltraces}(P) \mid \sigma \text{ is infinite}\}.$$

The fact that  $P$  is a process of type  $A$  is expressed by the notation  $P : A$ .

The most convenient way of defining the morphisms of  $\mathcal{SProc}$  is to define a  $*$ -autonomous structure on objects, and then say that the morphisms from  $A$  to  $B$  are processes of the internal hom type  $A \multimap B$ . This style of definition is typical of interaction categories; definitions of  $*$ -autonomous categories of games [8] follow the same pattern. Given objects  $A$  and  $B$ , the object  $A \otimes B$  has

$$\begin{aligned} \Sigma_{A \otimes B} &\stackrel{\text{def}}{=} \Sigma_A \times \Sigma_B \\ S_{A \otimes B} &\stackrel{\text{def}}{=} \{\sigma \in \Sigma_{A \otimes B}^* \mid \text{fst}^*(\sigma) \in S_A, \text{snd}^*(\sigma) \in S_B\}. \end{aligned}$$

The duality is trivial on objects:  $A^\perp \stackrel{\text{def}}{=} A$ . This means that at the level of types,  $\mathcal{SProc}$  makes no distinction between input and output. Because communication in  $\mathcal{SProc}$  consists of synchronisation rather than value-passing, processes do not distinguish between input and output either.

The definition of  $\otimes$  makes clear the extent to which processes in  $\mathcal{SProc}$  are synchronous. An action performed by a process of type  $A \otimes B$  consists of a pair of actions, one from the alphabet of  $A$  and one from that of  $B$ . Thinking of  $A$  and  $B$  as two ports of the process, synchrony means that at every time step a process must perform an action in every one of its ports.

For simplicity, we shall work with  $*$ -autonomous categories in which  $A^{\perp\perp} = A$ , and  $A \multimap B \stackrel{\text{def}}{=} (A \otimes B^\perp)^\perp$ ,  $A \wp B \stackrel{\text{def}}{=} (A^\perp \otimes B^\perp)^\perp$ . In  $\mathcal{SProc}$ , we have  $A = A^\perp$ , and hence  $A \wp B = A \multimap B = A \otimes B$ . Not all interaction categories exhibit this degeneracy of structure: in particular the category  $\mathcal{SProc}_D$  of deadlock-free processes, which will be defined in Section 6, gives distinct interpretations to  $\otimes$  and  $\wp$ .

A morphism  $p : A \rightarrow B$  of  $\mathcal{SProc}$  is a process  $p$  of type  $A \multimap B$  (so  $p$  has to satisfy a certain safety specification). Since  $A \multimap B = A \otimes B$  in  $\mathcal{SProc}$ , this amounts to saying that a morphism from  $A$  to  $B$  is a process of type  $A \otimes B$ . The reason for giving the definition in terms of  $\multimap$  is that it sets the pattern for all interaction category definitions, including cases in which there is less degeneracy.

If  $p : A \rightarrow B$  and  $q : B \rightarrow C$  then the composite  $p ; q : A \rightarrow C$  is defined by labelled transitions.

$$\frac{p \xrightarrow{(a,b)} p' \quad q \xrightarrow{(b,c)} q'}{p ; q \xrightarrow{(a,c)} p' ; q'}$$

At each step, the actions in the common type  $B$  have to match. The processes being composed constrain each other's behaviour, selecting the possibilities which agree in  $B$ . An example of composition is shown in Figure 1. This ongoing communication is the “interaction” of interaction categories. If the processes in the definition terminated after a single step, so that each could be considered simply as a set of pairs, then the labelled transition rule would reduce to precisely the definition of relational composition. This observation leads to the  $\mathcal{SProc}$  slogan: *processes are relations extended in time*.

The identity morphisms are synchronous buffers or wires: whatever is received by  $\text{id}_A : A \rightarrow A$  in the left copy of  $A$  is instantaneously transmitted to the right copy (and *vice versa*—there is no real directionality). The following auxiliary definition helps to define the identity processes. If  $P$  is a process with sort  $\Sigma$  and  $S \subseteq^{nepref} \Sigma^*$  then the process



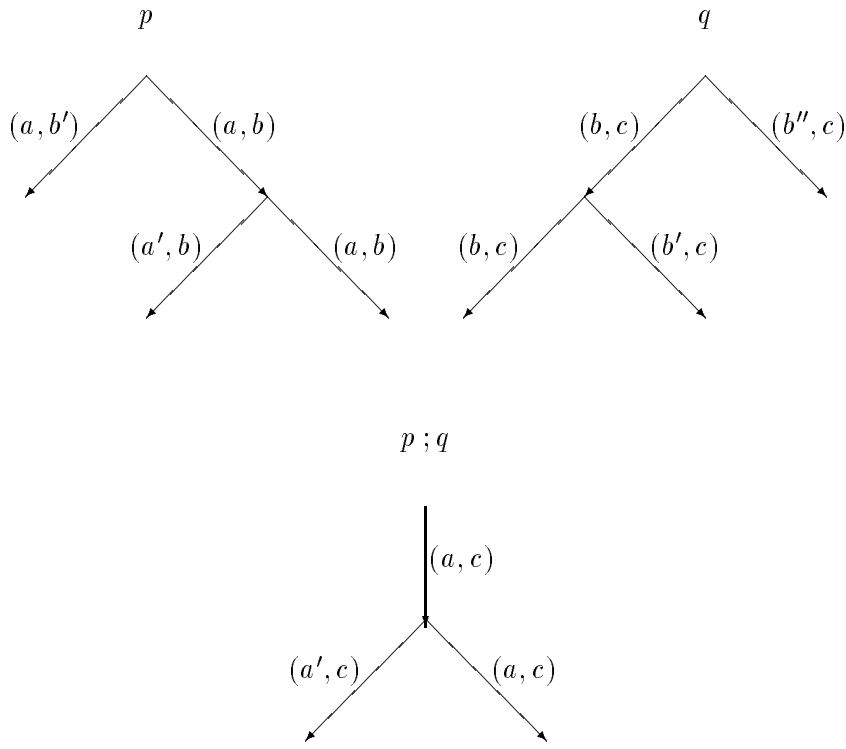


Figure 1: Composition in  $\mathcal{SProc}$

$P \downarrow S$ , also with sort  $\Sigma$ , is defined by the transition rule

$$\frac{P \xrightarrow{a} Q \quad a \in S}{P \downarrow S \xrightarrow{a} Q \downarrow (S/a)}$$

where  $S/a \stackrel{\text{def}}{=} \{\sigma \mid a\sigma \in S\}$ . Note that the condition  $a \in S$  in the transition rule refers to the singleton sequence  $a$  rather than the action  $a$ .

It is useful at this point to define the object  $A/s$ , where  $A$  is an object of  $\mathcal{SProc}$  and  $s \in S_A$ :

$$\begin{aligned} \Sigma_{A/s} &\stackrel{\text{def}}{=} \Sigma_A \\ S_{A/s} &\stackrel{\text{def}}{=} S_A/s. \end{aligned}$$

The identity morphism  $\text{id}_A : A \rightarrow A$  is defined by  $\text{id}_A \stackrel{\text{def}}{=} \text{id} \downarrow S_{A \rightarrow A}$  where the process  $\text{id}$  with sort  $\Sigma_{A \rightarrow A}$  is defined by the transition rule

$$\frac{a \in \Sigma_A}{\text{id} \xrightarrow{(a,a)} \text{id}}.$$

**Proposition 3.1**  *$\mathcal{SProc}$  is a category.*

**Proof** To prove that composition is associative and that identities work correctly, the strategy is to show that a suitable relation on processes is a bisimulation, and argue by coinduction. To prove that  $p ; (q ; r) = (p ; q) ; r$  for all  $p : A \rightarrow B$ ,  $q : B \rightarrow C$  and  $r : C \rightarrow D$ , the relation is

$$\{(p ; (q ; r), (p ; q) ; r) \mid p \in \text{ST}(\Sigma_{A \rightarrow B}), q \in \text{ST}(\Sigma_{B \rightarrow C}), r \in \text{ST}(\Sigma_{C \rightarrow D})\}.$$

To prove that  $p ; \text{id}_B = p$  for all  $p : A \rightarrow B$ , the relation is

$$\{(p ; \text{id}, p) \mid p \in \text{ST}(\Sigma_{A \rightarrow B})\}$$

where  $\text{id}$  has sort  $\Sigma_{B \multimap B}$ . In each case, the fact that the relation is a bisimulation follows easily from the transition rules defining composition.  $\square$

The following definition will be useful later.

**Definition 3.2** *If  $A$  is an object of  $\mathcal{SProc}$  and  $s \in S_A$ , then*

$$\Sigma_A(s) \stackrel{\text{def}}{=} \{a \in \Sigma_A \mid sa \in S_A\}.$$

### 3.1.1 $\mathcal{SProc}$ as a $*$ -Autonomous Category

The definitions of  $\otimes$  and  $(\Leftrightarrow)^\perp$  can now be extended to morphisms, making them into functors. If  $p : A \rightarrow C$  and  $q : B \rightarrow D$  then  $p \otimes q : A \otimes B \rightarrow C \otimes D$  and  $p^\perp : C^\perp \rightarrow A^\perp$  are defined by transition rules.

$$\frac{p \xrightarrow{(a,c)} p' \quad q \xrightarrow{(b,d)} q'}{p \otimes q \xrightarrow{((a,b),(c,d))} p' \otimes q'} \quad \frac{p \xrightarrow{(a,c)} p'}{p^\perp \xrightarrow{(c,a)} p'^\perp}$$

The tensor unit  $I$  is defined by

$$\Sigma_I \stackrel{\text{def}}{=} \{\bullet\} \quad S_I \stackrel{\text{def}}{=} \{\bullet^n \mid n < \omega\}.$$

The following notation provides a useful way of defining the structural morphisms needed to specify the rest of the  $*$ -autonomous structure. If  $P$  is a process with sort  $\Sigma$ , and  $f : \Sigma \rightarrow \Sigma'$  is a partial function, then  $P[f]$  is the process with sort  $\Sigma'$  defined by

$$\frac{P \xrightarrow{a} Q \quad a \in \text{dom}(f)}{P[f] \xrightarrow{f(a)} Q[f]}.$$

The canonical isomorphisms  $\text{unitl}_A : I \otimes A \cong A$ ,  $\text{unitr}_A : A \otimes I \cong A$ ,  $\text{assoc}_{A,B,C} : A \otimes (B \otimes C) \cong (A \otimes B) \otimes C$  and  $\text{symm}_{A,B} : A \otimes B \cong B \otimes A$  are defined below. We use a pattern-matching notation to define the partial functions needed for the relabelling operations; for example,  $(a, a) \mapsto ((\bullet, a), a)$  denotes the partial function which has the indicated effect when its arguments are equal.

$$\begin{aligned} \text{unitl}_A &\stackrel{\text{def}}{=} \text{id}_A[(a, a) \mapsto ((\bullet, a), a)] \\ \text{unitr}_A &\stackrel{\text{def}}{=} \text{id}_A[(a, a) \mapsto ((a, \bullet), a)] \\ \text{assoc}_{A,B,C} &\stackrel{\text{def}}{=} \text{id}_{A \otimes (B \otimes C)}[((a, (b, c)), (a, (b, c))) \mapsto ((a, (b, c)), ((a, b), c))] \\ \text{symm}_{A,B} &\stackrel{\text{def}}{=} \text{id}_{A \otimes B}[((a, b), (a, b)) \mapsto ((a, b), (b, a))]. \end{aligned}$$

If  $f : A \otimes B \rightarrow C$  then  $\Lambda(f) : A \rightarrow (B \multimap C)$  is defined by

$$\Lambda(f) \stackrel{\text{def}}{=} f[((a, b), c) \mapsto (a, (b, c))].$$

The evaluation morphism  $\text{Ap}_{A,B} : (A \multimap B) \otimes A \rightarrow B$  is defined by

$$\text{Ap}_{A,B} \stackrel{\text{def}}{=} \text{id}_{A \multimap B}[((a, b), (a, b)) \mapsto (((a, b), a), b)].$$

All of the structural morphisms are essentially formed from identities, and the only difference between  $f$  and  $\Lambda(f)$  is a reshuffling of ports.

If  $P$  is a process of type  $A$  then  $P[a \mapsto (\bullet, a)]$  is a morphism  $I \rightarrow A$  which can be identified with  $P$ . This agrees with the view of global elements (morphisms from  $I$ , in a  $*$ -autonomous category) as inhabitants of types.

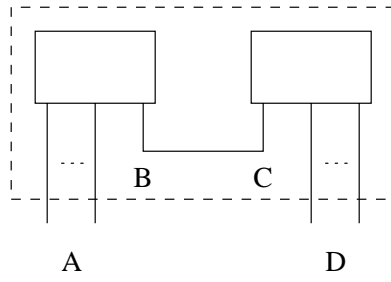


Figure 2: Using the Cut rule to connect modules

**Proposition 3.3**  *$\mathcal{S}Proc$  is a compact closed category.*

**Proof** Verifying the coherence conditions for  $\otimes$  is straightforward, given the nature of the canonical isomorphisms as relabelled identities. The properties required of  $\Lambda$  and  $\mathbf{A}p$  are equally easy to check. Since  $(\Leftrightarrow)^\perp$  is trivial, it is automatically an involution. This gives the  $*$ -autonomous structure; compact closure follows from the coincidence of  $\otimes$  and  $\wp$ .  $\square$

The following result on relabellings will be useful later.

**Lemma 3.4** *If  $p \in \mathbf{ST}(\Sigma_{A \rightarrow B})$ ,  $q \in \mathbf{ST}(\Sigma_{B \rightarrow C})$  and  $f : \Sigma_B \rightarrow \Sigma_D$  is an isomorphism, then*

$$p[(a, b) \mapsto (a, f(b))]; q[(b, c) \mapsto (f(b), c)] = p; q.$$

**Proof** It follows from the definitions of relabelling and composition that the relation

$$\{(p[(a, b) \mapsto (a, f(b))]; q[(b, c) \mapsto (f(b), c)], p; q) \mid p \in \mathbf{ST}(\Sigma_{A \rightarrow B}), q \in \mathbf{ST}(\Sigma_{B \rightarrow C})\}$$

is a bisimulation.  $\square$

### 3.1.2 Compact Closure and Multi-Cut

As we have already seen the linear type structure of  $\mathcal{S}Proc$  is quite degenerate. Specification structures can be used to enrich the specifications of  $\mathcal{S}Proc$  to stronger behavioural properties. This will have the effect of “sharpening up” the linear type structure so that the degeneracies disappear.

Our point here is that the looser type discipline of  $\mathcal{S}Proc$  can actually be *useful* in that it permits the flexible construction of a large class of processes within a typed framework. In particular, compact closure validates a very useful typing rule which we call *multi-cut*. (This is actually Gentzen’s MIX rule [22] but we avoid the use of this term since Girard has used it for a quite different rule in the context of linear logic.)

The usual Cut rule

$$\frac{\vdash \Gamma, A \quad \vdash \Delta, A^\perp}{\vdash \Gamma, \Delta}$$

allows us to plug two modules together by an interface consisting of a single port [7], as in Figure 2. This allows us to connect processes in a tree structure, as in Figure 3(a), but not to construct cyclic interconnection networks as in Figure 3(b). The problem with constructing a cycle occurs at the final step, when two processes must be plugged together on two ports simultaneously as in Figure 3(c). Cyclic connections would be supported if

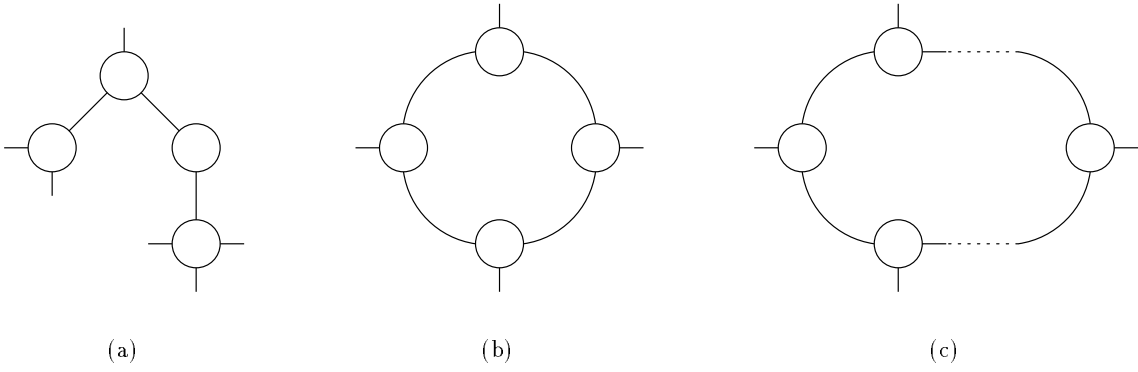


Figure 3: Cyclic and acyclic networks

we had the following binary version of the Cut rule:

$$\frac{\vdash \gamma, A_1, A_2 \quad \vdash \Delta, A_1^\perp, A_2^\perp}{\vdash \gamma, \Delta}$$

or more generally the “multi-cut” rule:

$$\frac{\vdash \gamma, \Delta \quad \vdash \gamma', \Delta^\perp}{\vdash \gamma, \gamma'}$$

This rule is not admissible in Linear Logic and cannot in general be interpreted in  $\ast$ -autonomous categories. However it can always be canonically interpreted in a compact closed category (and hence in particular in  $\mathcal{S}Proc$ ) as the following construction shows.

Let  $\gamma = A_1, \dots, A_m$ ,  $\gamma' = B_1, \dots, B_n$ ,  $\Delta = C_1, \dots, C_k$  and write

$$\begin{aligned} \tilde{A} &= A_1 \otimes \dots \otimes A_m, & \tilde{B} &= B_1 \otimes \dots \otimes B_n, & \tilde{C} &= C_1 \otimes \dots \otimes C_k \\ \tilde{C}^\perp &= (C_1 \otimes \dots \otimes C_k)^\perp \cong C_1^\perp \otimes \dots \otimes C_k^\perp. \end{aligned}$$

Suppose that the proofs of  $\vdash \gamma, \Delta$  and  $\vdash \gamma', \Delta'$  are interpreted by morphisms

$$f : I \rightarrow \tilde{A} \otimes \tilde{C} \quad g : I \rightarrow \tilde{B} \otimes \tilde{C}^\perp$$

respectively. Then we can construct the required morphism  $h : I \rightarrow \tilde{A} \otimes \tilde{B}$  as follows.

$$\begin{array}{ccccc} I & \xrightarrow{\sim} & I \otimes I & \xrightarrow{f \otimes g} & (\tilde{A} \otimes \tilde{C}) \otimes (\tilde{B} \otimes \tilde{C}^\perp) \\ \downarrow h & & & & \downarrow \sim \\ \tilde{A} \otimes \tilde{B} & \xleftarrow{\sim} & \tilde{A} \otimes I \otimes \dots \otimes I \otimes \tilde{B} & \xleftarrow{\text{evaluate}} & \tilde{A} \otimes (C_1 \otimes C_1^\perp) \otimes \dots \otimes (C_k \otimes C_k^\perp) \otimes \tilde{B} \end{array}$$

Note that in a compact closed category  $I = \perp$  so  $A^\perp = A \multimap I$ . Arrows labelled by  $\sim$  are canonical isomorphisms, and the morphism **evaluate** is  $\text{id} \otimes \text{Ap} \otimes \dots \otimes \text{Ap} \otimes \text{id}$ .

In the case where  $k = 1$  this construction is the internalization of composition in the category (using the autonomous structure) so it properly generalizes the standard interpretation of Cut. Some related notions, arising in work on coherence in compact closed categories, can be found in the literature [15, 29].

### 3.2 Products, Coproducts and Non-determinism

The binary coproduct functor  $\oplus$  is defined on objects by

$$\begin{aligned}\Sigma_{A \oplus B} &\stackrel{\text{def}}{=} \Sigma_A + \Sigma_B \\ S_{A \oplus B} &\stackrel{\text{def}}{=} \{\text{inl}^*(s) \mid s \in S_A\} \\ &\cup \{\text{inr}^*(s) \mid s \in S_B\}.\end{aligned}$$

If  $p : A \rightarrow C$  and  $q : B \rightarrow D$  then  $p \oplus q : A \oplus B \rightarrow C \oplus D$  is defined by

$$\begin{aligned}p \oplus q &\stackrel{\text{def}}{=} p[(a, c) \mapsto (\text{inl}(a), \text{inl}(c))] \\ &+ q[(b, d) \mapsto (\text{inr}(b), \text{inr}(d))].\end{aligned}$$

The insertions  $\text{inl}_{A,B} : A \rightarrow A \oplus B$  and  $\text{inr}_{A,B} : B \rightarrow A \oplus B$  are defined by

$$\begin{aligned}\text{inl}_{A,B} &\stackrel{\text{def}}{=} \text{id}_A[(a, a) \mapsto (a, \text{inl}(a))] \\ \text{inr}_{A,B} &\stackrel{\text{def}}{=} \text{id}_B[(b, b) \mapsto (b, \text{inr}(b))]\end{aligned}$$

and, for  $p : A \rightarrow C$ ,  $q : B \rightarrow C$ ,  $[p, q] : A \oplus B \rightarrow C$  is

$$\begin{aligned}[p, q] &\stackrel{\text{def}}{=} p[(a, c) \mapsto (\text{inl}(a), c)] \\ &+ q[(b, c) \mapsto (\text{inr}(b), c)].\end{aligned}$$

In these definitions, the operation  $+$  on processes means non-deterministic sum of labelled transition systems (i.e. the standard CCS  $+$ ).

**Proposition 3.5** *The above definitions make  $A \oplus B$  a coproduct of  $A$  and  $B$ .*

**Proof** Suppose  $p : A \rightarrow C$  and  $q : B \rightarrow C$ . We first need to check that  $\text{inl} ; [p, q] = p$ . The definitions of  $[p, q]$  and composition mean that

$$\text{inl} ; [p, q] \xrightarrow{(a,c)} \text{inl} ; p'[(a, c) \mapsto (\text{inl}(a), c)] \iff p \xrightarrow{(a,c)} p'.$$

Since  $\text{inl} = \text{id}[(a, a) \mapsto (a, \text{inl}(a))]$ , Lemma 3.4 shows that

$$\text{inl} ; p'[(a, c) \mapsto (\text{inl}(a), c)] = \text{id} ; p' = p'.$$

Hence  $\text{inl} ; [p, q]$  and  $p$  are bisimilar.

Symmetrically,  $\text{inr} ; [p, q] = q$ .

Now suppose that  $h : A \oplus B \rightarrow C$  with  $\text{inl} ; h = p$  and  $\text{inr} ; h = q$ . There is a trivial possibility to dispose of: if  $h = \text{nil}$  then  $p = \text{nil}$  and  $q = \text{nil}$ , and  $[p, q] = \text{nil} = h$ .

Otherwise, the type of  $h$  means that its first transition has one of two forms: either

$$h \xrightarrow{(\text{inl}(a), c)} h'$$

or

$$h \xrightarrow{(\text{inr}(b), c)} h'.$$

In the first case, because  $\text{inl} ; h = p$ , we have  $p \xrightarrow{(a,c)} p'$  with  $\text{inl} ; h' = p'$ . The safety specification of  $A \oplus B$  means that we can consider  $h'$  as a morphism  $\text{inl}(A) \rightarrow C$ , where the object  $\text{inl}(A)$  is defined by

$$\begin{aligned}\Sigma_{\text{inl}(A)} &\stackrel{\text{def}}{=} \{\text{inl}(a) \mid a \in \Sigma_A\} \\ S_{\text{inl}(A)} &\stackrel{\text{def}}{=} \{\text{inl}^*(s) \mid s \in S_A\}.\end{aligned}$$

Now we have

$$\begin{aligned}
p'[(a, c) \mapsto (\text{inl}(a), c)] &= (\text{inl}; h')[(a, c) \mapsto (\text{inl}(a), c)] \\
&= \text{inl}[(a, \text{inl}(a)) \mapsto (\text{inl}(a), \text{inl}(a))]; h' \\
&= \text{id}_{\text{inl}(A)}; h' \\
&= h'.
\end{aligned}$$

Similarly, in the second case we have  $q \xrightarrow{(b,c)} q'$  and  $h' = q'[(b, c) \mapsto (\text{inr}(b), c)]$ .

Finally, note that the first transition of  $h$  is the same as that of either  $p[(a, c) \mapsto (\text{inl}(a), c)]$  or  $q[(b, c) \mapsto (\text{inr}(b), c)]$  as appropriate. Hence  $h$  is bisimilar to  $p[(a, c) \mapsto (\text{inl}(a), c)] + q[(b, c) \mapsto (\text{inr}(b), c)]$ , which is the definition of  $[p, q]$ .  $\square$

Since  $\oplus$  is a coproduct, its dual is a product; because all objects of  $\mathcal{S}Proc$  are self-dual, this means that  $A \oplus B$  is itself also a product of  $A$  and  $B$ —so, in fact, a biproduct.

We will use the notation  $A \& B$  for the product of  $A$  and  $B$ , in line with the standard notation for the additive connectives of linear logic [23]. In  $\mathcal{S}Proc$ ,  $A \& B$  is the same object as  $A \oplus B$ , but we will use the product notation when we want to emphasise the product properties. Exploiting the self-duality of  $\mathcal{S}Proc$  objects, we can define projections and pairing as follows.

$$\begin{aligned}
\pi_A &\stackrel{\text{def}}{=} \text{inl}^\perp \\
\pi_B &\stackrel{\text{def}}{=} \text{inr}^\perp \\
\langle p, q \rangle &\stackrel{\text{def}}{=} [p, q]^\perp
\end{aligned}$$

There is also a zero object  $\mathbf{0}$  which has  $\Sigma_{\mathbf{0}} \stackrel{\text{def}}{=} \emptyset$  and  $S_{\mathbf{0}} \stackrel{\text{def}}{=} \{\varepsilon\}$ .

**Proposition 3.6** *The object  $\mathbf{0}$  is initial and terminal in  $\mathcal{S}Proc$ .*

**Proof** The only safe trace for  $\mathbf{0}$  is the empty trace, so a morphism  $A \rightarrow \mathbf{0}$  cannot make any transitions and must be  $\text{nil}$ . Similarly for a morphism  $\mathbf{0} \rightarrow A$ .  $\square$

When a category has biproducts and a zero object, it is possible to define a commutative monoid structure on each homset [33]. If  $p, q : A \rightarrow B$  then  $p + q : A \rightarrow B$  is defined by

$$\begin{aligned}
p + q &\stackrel{\text{def}}{=} A \xrightarrow{\Delta_A} A \oplus A \xrightarrow{[p,q]} B \\
&= A \xrightarrow{\langle p,q \rangle} B \oplus B \xrightarrow{\nabla_B} B
\end{aligned}$$

where  $\Delta_A \stackrel{\text{def}}{=} \langle \text{id}_A, \text{id}_A \rangle$  is the diagonal and  $\nabla_B \stackrel{\text{def}}{=} [\text{id}_B, \text{id}_B]$  the codiagonal. The unit is defined by  $0_{A \rightarrow B} \stackrel{\text{def}}{=} A \rightarrow \mathbf{0} \rightarrow B$ .

In  $\mathcal{S}Proc$ , this construction yields the non-deterministic sum of CCS (when strong bisimulation is taken as the notion of equivalence). The proof of Proposition 3.6 shows that the unique morphisms into and out of  $\mathbf{0}$  are  $\text{nil}$  processes, and so  $0_A$  is also  $\text{nil}$ . To unravel the definition of  $+$ , consider the composition  $\langle p, q \rangle; \nabla_B$ . Pairing creates a union of the behaviours of  $p$  and  $q$ , but with disjointly labelled copies of  $B$ . Composing with  $\nabla_B$  removes the difference between the two copies. A choice can be made between  $p$  and  $q$  at the first step, but then the behaviour continues as behaviour of  $p$  or behaviour of  $q$ .

### 3.2.1 Time

So far, all of the constructions in  $\mathcal{S}Proc$  have been essentially constructions on relations, extended uniformly through time. The next step is to define an operator which allow the temporal structure of the morphisms to be manipulated.

The basic construction dealing with time is the unit delay functor  $\circ$ . It is defined on objects by

$$\begin{aligned}\Sigma_{\circ A} &\stackrel{\text{def}}{=} \{*\} + \Sigma_A \\ S_{\circ A} &\stackrel{\text{def}}{=} \{\varepsilon\} \cup \{*\sigma \mid \sigma \in S_A\}.\end{aligned}$$

It is notationally convenient to write  $*$  instead of  $\text{inl}(*)$ , assuming that  $*$   $\notin \Sigma_A$ . Given  $f : A \rightarrow B$ ,  $\circ f : \circ A \rightarrow \circ B$  is defined by the single transition  $\circ f \xrightarrow{(*,*)} f$ .

It is straightforward to check that  $\circ$  is indeed a functor. In fact it is a strict monoidal functor.

**Proposition 3.7** *There are isomorphisms*

$$\mathbf{mon}_{A,B} : (\circ A) \otimes (\circ B) \rightarrow \circ(A \otimes B)$$

(natural in  $A$  and  $B$ ) and  $\mathbf{monunit} : I \rightarrow \circ I$ .

**Proof**  $\mathbf{monunit} : I \cong \circ I$  is defined by  $\mathbf{monunit} \xrightarrow{(*,*)} \text{id}_I$ .

$\mathbf{mon}_{A,B} : (\circ A) \otimes (\circ B) \cong \circ(A \otimes B)$  is defined by  $\mathbf{mon}_{A,B} \xrightarrow{((*,*),*)} \text{id}_{A \otimes B}$ .

In both cases the inverse is obtained by considering the process as a morphism in the opposite direction. It is easy to check that these are isomorphisms and that  $\mathbf{mon}$  is natural.  $\square$

The most important feature of  $\circ$  is that it has the following *unique fixed point property* (UFPP) [1].

**Proposition 3.8** *For any objects  $A$  and  $B$ , and any morphisms  $f : A \rightarrow \circ A$  and  $g : \circ B \rightarrow B$ , there is a unique morphism  $It(f, g) : A \rightarrow B$  such that*

$$\begin{array}{ccc} A & \xrightarrow{f} & \circ A \\ \text{It}(f, g) \downarrow & & \downarrow \circ \text{It}(f, g) \\ B & \xleftarrow{g} & \circ B \end{array}$$

*commutes.*

**Proof** The equational condition that the square commute, namely  $It(f, g) = f ; \circ It(f, g) ; g$ , can be read as a guarded recursive definition of  $It(f, g)$ . It is standard in concurrency theory that such a definition has a unique solution [36].  $\square$

We will not go into the applications of this property in the present paper, except to mention that it supports guarded recursive definitions [1, 21] and is an important part of a proposed axiomatisation of interaction categories [21]. The notation  $It(f, g)$  is intended to suggest *iteration*.

## 4 Specification Structures over Interaction Categories

### 4.1 The Sequence of Definitions

Suppose  $\mathcal{C}$  is a  $*$ -autonomous category with a notion of a set of *processes* of each type, written  $\text{Proc}(A)$  (a process  $P$  of type  $A$  may be identified with a morphism  $P : I \rightarrow A$ ). The

following sequence of steps provides a convenient way to define a specification structure  $S$ . This sequence will be used in the present paper when defining specification structures over  $\mathcal{S}Proc$ ; it mirrors the sequence already used in the definition of  $\mathcal{S}Proc$  itself.

1. Define  $P_S A$  for each  $A$ .
2. For each  $A$ , define a relation of *satisfaction*:  $\models_A \subseteq \mathbf{Proc}(A) \times P_S A$ .
3. Define  $(\Leftrightarrow)_A^\perp$ .
4. Define  $\otimes_{A,B}$  and hence  $\wp_{A,B}$  and  $\multimap_{A,B}$ .
5. Define the Hoare triple relation by  $\theta\{f\}\varphi \stackrel{\text{def}}{\Leftrightarrow} f \models_{A \multimap B} \theta \multimap \varphi$ .
6. Verify that the desired structure of  $\mathcal{C}$ , including the  $*$ -autonomous structure, lifts to  $\mathcal{C}_S$ .

For reference, we will now list the definitions and conditions which are needed to lift the relevant structure of  $\mathcal{C}$  to  $\mathcal{C}_S$ . In the present paper, we are interested in the  $*$ -autonomous structure, products and coproducts, the unit delay functor, and the UFPP.

## 4.2 $*$ -Autonomous Structure

For every pair  $A, B$  of objects we need an operation

$$\otimes_{A,B} : P_S A \times P_S B \rightarrow P_S(A \otimes B).$$

When writing this and similar operations, we will usually omit the subscripts. To define the functor  $\otimes : \mathcal{C}_S \times \mathcal{C}_S \rightarrow \mathcal{C}_S$  we need, for every  $\theta, \varphi, \psi, \rho \in P_S A, P_S B, P_S C, P_S D$ ,  $f : A \rightarrow C$  and  $g : B \rightarrow D$ ,

$$\theta\{f\}\psi, \varphi\{g\}\rho \Rightarrow (\theta \otimes \varphi)\{f \otimes g\}(\psi \otimes \rho).$$

Then

$$(A, \theta) \otimes (B, \varphi) \stackrel{\text{def}}{=} (A \otimes B, \theta \otimes_{A,B} \varphi).$$

We need  $I_S \in P_S I$  in order to define the tensor unit in  $\mathcal{C}_S$  by  $(I, I_S)$ .

To lift the symmetric monoidal structure of  $\mathcal{C}$  to  $\mathcal{C}_S$  we need the following conditions, for every  $\theta, \varphi, \psi \in P_S A, P_S B, P_S C$ .

$$\begin{aligned} & (\theta \otimes (\varphi \otimes \psi))\{\text{assoc}_{A,B,C}\}((\theta \otimes \varphi) \otimes \psi) \\ & \quad (I_S \otimes \theta)\{\text{unitl}_A\}\theta \\ & \quad (\theta \otimes I_S)\{\text{unitr}_A\}\theta \\ & (\theta \otimes \varphi)\{\text{symm}_{A,B}\}(\varphi \otimes \theta) \end{aligned}$$

For each object  $A$  we need an operation

$$(\Leftrightarrow)_A^\perp : P_S A \rightarrow P_S(A^\perp)$$

and we can then define

$$(A, \theta)^\perp \stackrel{\text{def}}{=} (A^\perp, \theta_A^\perp).$$

In order to define the functorial action of  $(\Leftrightarrow)^\perp$  on  $\mathcal{C}_S$  we need, for every  $\theta, \varphi \in P_S A, P_S B$  and  $f : A \rightarrow B$ ,

$$\theta\{f\}\varphi \Rightarrow \varphi_B^\perp\{f^\perp\}\theta_A^\perp.$$



The operations

$$\begin{aligned}\wp_{A,B} &: P_S A \times P_S B \rightarrow P_S(A \wp B) \\ \dashv_{A,B} &: P_S A \times P_S B \rightarrow P_S(A \dashv B)\end{aligned}$$

can be defined by

$$\begin{aligned}\theta \wp_{A,B} \varphi &\stackrel{\text{def}}{=} (\theta_A^\perp \otimes_{A^\perp, B^\perp} \varphi_B^\perp)_{A^\perp \otimes B^\perp}^\perp \\ \theta \dashv_{A,B} \varphi &\stackrel{\text{def}}{=} (\theta \otimes_{A, B^\perp} \varphi_B^\perp)_{A \otimes B^\perp}^\perp\end{aligned}$$

and the property  $\perp_S$  by

$$\perp_S \stackrel{\text{def}}{=} I_S^\perp.$$

To lift the closed structure we need, for every  $\theta, \varphi, \psi \in P_S A, P_S B, P_S C$  and  $f : A \otimes B \rightarrow C$ ,

$$(\theta \otimes \varphi)\{f\}\psi \Rightarrow \theta\{\Lambda(f)\}(\varphi \dashv \psi)$$

and

$$((\theta \dashv \varphi) \otimes \theta)\{\mathbf{Ap}_{A,B}\}\varphi.$$

### 4.3 Products and Coproducts

For every pair  $A, B$  of objects we need an operation

$$\&_{A,B} : P_S A \times P_S B \rightarrow P_S(A \& B).$$

This enables the product in  $\mathcal{C}_S$  to be defined on objects by

$$(A, \theta) \& (B, \varphi) \stackrel{\text{def}}{=} (A \& B, \theta \&_{A,B} \varphi).$$

For functoriality, it must be the case that for every  $\theta, \varphi, \psi, \rho \in P_S A, P_S B, P_S C, P_S D$ ,  $f : A \rightarrow C$  and  $g : B \rightarrow D$ ,

$$\theta\{f\}\psi, \varphi\{g\}\rho \Rightarrow (\theta \& \varphi)\{f \& g\}(\psi \& \rho).$$

Additionally, we need

$$\begin{aligned}(\theta \& \varphi)\{\pi_A\}\theta \\ (\theta \& \varphi)\{\pi_B\}\varphi\end{aligned}$$

and, for  $f : C \rightarrow A$ ,  $g : C \rightarrow B$ ,

$$\psi\{f\}\theta, \psi\{g\}\varphi \Rightarrow \psi\{\langle f, g \rangle\}(\theta \& \varphi).$$

The treatment of coproducts is dual.

### 4.4 Unit Delay

For each object  $A$  we need an operation

$$\circ_A : P_S A \rightarrow P_S(\circ A)$$

in order to define the unit delay on  $\mathcal{C}_S$  by

$$\circ(A, \theta) \stackrel{\text{def}}{=} (\circ A, \circ_A \theta).$$

For functoriality we need, for each  $f : A \rightarrow B$  and  $\theta, \varphi \in P_S A, P_S B$ ,

$$\theta\{f\}\varphi \Rightarrow \circ\theta\{\circ f\}\circ\varphi.$$

Lifting the UFPP to  $\mathcal{C}_S$  requires that if  $f : A \rightarrow \circ A$  and  $g : \circ B \rightarrow B$  with  $\theta, \varphi \in P_S A, P_S B$ ,

$$\theta\{f\}\circ\theta, \circ\varphi\{g\}\varphi \Rightarrow \theta\{It(f, g)\}\varphi.$$

## 5 A Specification Structure for Deadlock-Freedom

Throughout this paper, deadlock means termination. A more refined treatment might consider *unsuccessful* termination as deadlock; the view taken here is that all termination is unsuccessful. In fact, given the synchronous nature of  $\mathcal{S}Proc$ , successful termination is not especially interesting because all processes would have to terminate simultaneously. A process may have both terminating and non-terminating behaviours, but a *deadlock-free* process is one which has no maximal finite behaviours. For example, the process  $a.b.nil$  can deadlock; the process  $P$  defined by  $P = a.P + b.nil$  can deadlock although it can also generate the infinite trace  $a^\omega$ ; the process  $Q$  defined by  $Q = a.b.Q$  is deadlock-free.

Deadlock-freedom is not preserved by composition: two processes may individually be deadlock-free, but when forced to communicate they could deadlock each other by being unable to agree on a sequence of actions to perform. For example, if the CCS processes  $P$  and  $Q$  are defined by  $P = a.b.P$  and  $Q = \bar{a}.c.Q$  then composing them means forming the process  $(P \mid Q) \setminus \{a, b, c\}$ . In this process,  $P$  and  $Q$  can communicate for a single step, but then deadlock occurs because  $P$  must do  $b$  next while  $Q$  can only do  $c$ .

In order to construct a category of deadlock-free processes which are guaranteed to remain deadlock-free when composed with each other, more information is needed than just the fact that a process runs forever. The rest of this section describes one approach to building suitable extra information into the types, by constructing a specification structure over  $\mathcal{S}Proc$ . In Section 6 we define an alternative specification structure, and prove that the two are equivalent.

The first construction of a specification structure for deadlock-freedom takes a property over a type to be a set of processes of that type. This is clearly the most general notion of property, and has no inherent connection with deadlocks. For these properties to say anything about deadlock-freedom, the sets of processes must be carefully chosen in a way which will now be described.

### 5.1 Fundamentals

**Definition 5.1** *A process  $P$  of type  $A$  converges, written  $P \downarrow$ , if whenever  $P \xrightarrow{s}^* Q$  there is  $a \in \Sigma_A$  and a process  $R$  such that  $Q \xrightarrow{a} R$ .*

Convergence means deadlock-freedom; the reason for the choice of terminology is an analogy with proofs of strong normalisation in Classical Linear Logic [23, 4].

**Definition 5.2** *Given processes  $P$  and  $Q$  of type  $A$ , the process  $P \sqcap Q$  of type  $A$  is defined by*

$$\frac{P \xrightarrow{a} P' \quad Q \xrightarrow{a} Q'}{P \sqcap Q \xrightarrow{a} P' \sqcap Q'}.$$

*For each type  $A$ , the orthogonality relation on the set of processes of type  $A$  is defined by*

$$P \perp Q \stackrel{\text{def}}{=} (P \sqcap Q) \downarrow.$$

If  $P$  and  $Q$  are orthogonal, then they represent possible projections, in the shared type, of two successfully communicating processes. For example, if  $R : A \rightarrow B$  and  $S : B \rightarrow C$  then the behaviours of  $R$  and  $S$  in their ports of type  $B$  can be described as processes  $P$  and  $Q$  of type  $B$ ; orthogonality of  $P$  and  $Q$  corresponds to non-deadlocking communication between  $R$  and  $S$  when they are combined into  $R ; S$ .

Because only deadlock-free processes are of interest in this section, it is convenient to restrict attention to those types of  $\mathcal{S}Proc$  whose safety specifications do not force termination. Such types are called *progressive*.

**Definition 5.3** *An object  $A$  of  $\mathcal{S}Proc$  is progressive if*

$$\forall s \in S_A. \exists a \in \Sigma_A. sa \in S_A.$$

The full subcategory of  $\mathcal{S}Proc$  consisting of just the progressive objects is denoted by  $\mathcal{S}Proc_{pr}$ .

$\mathcal{S}Proc_{pr}$  inherits all the structure of  $\mathcal{S}Proc$ , apart from the zero object. The specification structure for deadlock-freedom will be defined over  $\mathcal{S}Proc_{pr}$ .

**Definition 5.4** *For each object  $A$  of  $\mathcal{S}Proc_{pr}$ ,  $\mathbf{Proc}(A)$  is the set of convergent processes of type  $A$ . The orthogonality relation is extended to sets of processes by defining, for  $U, V \subseteq \mathbf{Proc}(A)$  and  $P : A$ ,*

$$\begin{aligned} P \perp U &\stackrel{\text{def}}{\iff} \forall Q \in U. P \perp Q \\ U \perp V &\stackrel{\text{def}}{\iff} \forall P \in U. P \perp V. \end{aligned}$$

Orthogonality then generates an operation of negation on sets of processes, defined by

$$U^\perp \stackrel{\text{def}}{=} \{P \in \mathbf{Proc}(A) \mid P \perp U\}.$$

Defining  $(\Leftrightarrow)^\perp$  in this way from a symmetric orthogonality relation yields a self-adjoint Galois connection [17] and the following lemma holds for general reasons.

**Lemma 5.5** *For all  $U, V \subseteq \mathbf{Proc}(A)$ ,*

1.  $U \subseteq V \Rightarrow V^\perp \subseteq U^\perp$
2.  $U \subseteq U^{\perp\perp}$
3.  $U^\perp = U^{\perp\perp\perp}$ .

**Proof**

1. If  $P \in V^\perp$  then  $\forall Q \in V. P \perp Q$ . Hence  $\forall Q \in U. P \perp Q$ . Hence  $P \in U^\perp$ .
2. If  $P \in U$  and  $Q \in U^\perp$  then  $P \perp Q$ . Hence  $\forall Q \in U^\perp. P \perp Q$ . Hence  $P \in U^{\perp\perp}$ .
3. From 2 we have  $U^\perp \subseteq U^{\perp\perp\perp}$ . Also, applying 1 to 2 gives  $U^{\perp\perp\perp} \subseteq U^\perp$ .  $\square$

We will also need some additional results.

**Definition 5.6** *For any object  $A$  of  $\mathcal{S}Proc_{pr}$ , the process  $\mathbf{max}_A : A$  is defined by*

$$\frac{a \in S_A}{\mathbf{max}_A \xrightarrow{a} \mathbf{max}_{A/a}}.$$

**Lemma 5.7** *For any  $P \in \mathbf{Proc}(A)$ ,  $P \perp \mathbf{max}_A$ .*

**Proof**  $P \sqcap \mathbf{max}_A = P$ , so  $P \perp \mathbf{max}_A$  because  $P \downarrow$ .  $\square$

**Lemma 5.8** *If  $U \subseteq \mathbf{Proc}(A)$  then  $U^\perp \neq \emptyset$ .*

**Proof** Lemma 5.7 implies that  $\mathbf{max}_A \in U^\perp$ .  $\square$

## 5.2 The Specification Structure

Following the sequence of definitions listed in Section 4, we can now define the specification structure  $D$ .

### Definition 5.9

1.  $P_DA \stackrel{\text{def}}{=} \{U \subseteq \text{Proc}(A) \mid (U \neq \emptyset) \wedge (U^{\perp\perp} = U)\}$ .
2.  $P \models U \stackrel{\text{def}}{\iff} P \in U$ .
3.  $(\iff)^\perp : P_DA \rightarrow P_DA$  has already been defined.
- 4.

$$\begin{aligned} U \otimes V &\stackrel{\text{def}}{=} \{P \otimes Q \mid P \in U, Q \in V\}^{\perp\perp} \\ U \wp V &\stackrel{\text{def}}{=} (U^\perp \otimes V^\perp)^\perp \\ U \multimap V &\stackrel{\text{def}}{=} (U \otimes V^\perp)^\perp \\ I_D &\stackrel{\text{def}}{=} \{\max_I\}. \end{aligned}$$

5.  $U\{f\}V \stackrel{\text{def}}{\iff} f \models U \multimap V$ .
6. The definitions required to lift the additive and temporal structure of  $\mathcal{S}\text{Proc}$  to  $\mathcal{S}\text{Proc}_D$  are

$$\begin{aligned} U \oplus V &\stackrel{\text{def}}{=} (\{P[\text{inl}] \mid P \in U\} \cup \{Q[\text{inr}] \mid Q \in V\})^{\perp\perp} \\ U \& V &\stackrel{\text{def}}{=} (U^\perp \oplus V^\perp)^\perp \\ \circ U &\stackrel{\text{def}}{=} \{\circ P \mid P \in U\}. \end{aligned}$$

The following points are worth noting.

- Lemma 5.8 ensures that clause 3 of the definition makes sense, by guaranteeing that  $U^\perp \in P_DA$  for each  $U \in P_DA$ .
- For any  $U \subseteq \text{Proc}(A)$ ,  $U^{\perp\perp}$  is the smallest  $^{\perp\perp}$ -invariant set of processes containing  $U$ .
- There are now separate definitions relating to product ( $\&$ ) and coproduct ( $\oplus$ ). We will prove later that these connectives are distinct in the specification structure  $D$ .

We need to check that every set of processes defined in Definition 5.9 is  $^{\perp\perp}$ -invariant. In every case except that of  $\circ$ , this follows from the fact (Lemma 5.5) that for every  $U \subseteq \text{Proc}(A)$ ,  $U^\perp$  is  $^{\perp\perp}$ -invariant.

**Lemma 5.10** *If  $U \in P_DA$  then  $\circ U \in P_D(\circ A)$ .*

**Proof** First,  $\circ U \neq \emptyset$  because  $U \neq \emptyset$ .

Next, note that for  $P, Q \in \text{Proc}(\circ A)$ , there are  $P', Q' \in \text{Proc}(A)$  such that  $P = \circ P'$  and  $Q = \circ Q'$ . Furthermore,  $P \perp Q \iff P' \perp Q'$ .

This means that

$$\begin{aligned} (\circ U)^\perp &= \{\circ P \mid P \in U\}^\perp \\ &= \{\circ Q \mid Q \in U^\perp\} \\ &= \circ(U^\perp) \end{aligned}$$

and hence  $(\circ U)^{\perp\perp} = \circ(U^{\perp\perp}) = \circ U$ . □

We can now prove that  $D$  satisfies the specification structure axioms.

**Lemma 5.11** *If  $U \subseteq V \subseteq \text{Proc}(A)$  then  $\text{id}_A \in U \multimap V$ .*

**Proof** We need  $\text{id}_A \in (U \otimes V^\perp)^\perp$ . Now,

$$\begin{aligned} (U \otimes V^\perp)^\perp &= \{P \otimes Q \mid P \in U, Q \in V^\perp\}^{\perp\perp\perp} \\ &= \{P \otimes Q \mid P \in U, Q \in V^\perp\}^\perp \end{aligned}$$

so it is enough to show  $\text{id}_A \perp \{P \otimes Q \mid P \in U, Q \in V^\perp\}$ . Let  $P \in U$  and  $Q \in V^\perp$ .  $U \subseteq V$  implies  $V^\perp \subseteq U^\perp$ , so  $Q \in U^\perp$  and hence  $P \perp Q$ . For any common trace  $s$  of  $\text{id}_A$  and  $P \otimes Q$ ,  $\text{fst}^*(s)$  is a trace of  $P$  and  $\text{snd}^*(s)$  is a trace of  $Q$ , and  $\text{fst}^*(s) = \text{snd}^*(s)$ . So there is an action  $a$  such that  $\text{fst}^*(s)a$  is a trace of  $P$  and  $\text{snd}^*(s)a$  is a trace of  $Q$ . Hence  $(a, a)$  is an action such that  $s(a, a)$  is a trace of both  $\text{id}_A$  and  $P \otimes Q$ . This means that  $(\text{id}_A \sqcap (P \otimes Q)) \downarrow$ , and so  $\text{id}_A \perp P \otimes Q$ .  $\square$

For the next two lemmas, a slight abuse of notation is useful. If  $f : A \rightarrow B$  and  $P : A$ , there is a process  $P ; f$  of type  $B$  obtained by regarding  $P$  as a morphism  $I \rightarrow A$ , composing with  $f$ , and then regarding the resulting morphism  $I \rightarrow B$  as a process of type  $B$ . Similarly, if  $Q : B^\perp$  there is a process  $f ; Q$  of type  $A$ .

**Lemma 5.12** *If  $f : A \rightarrow B$ ,  $U \in P_D A$ ,  $V \in P_D B$ ,  $f \in U \multimap V$  and  $P \in U$ , then  $P ; f \in V$ .*

**Proof** To show that  $P ; f \in V$ , we will consider an arbitrary  $Q \in V^\perp$  and show that  $P ; f \perp Q$ , thus establishing  $P ; f \in V^{\perp\perp} = V$ .

Let  $Q \in V^\perp$  and let  $s$  be a common trace of  $P ; f$  and  $Q$ . The definition of composition means that there is a trace  $t$  of  $f$  such that  $\text{fst}^*(t)$  is a trace of  $P$  and  $\text{snd}^*(t) = s$ . We have  $f \in U \multimap V$ , and so  $f \perp (P \otimes Q)$ . Hence there is an action  $(a, b)$  such that  $t(a, b)$  is a trace of  $f$ ,  $\text{fst}^*(t)a$  is a trace of  $P$  and  $\text{snd}^*(t)b$  is a trace of  $Q$ . Then  $sb$  is a common trace of  $P ; f$  and  $Q$ , so  $((P ; f) \sqcap Q) \downarrow$  as required.  $\square$

**Lemma 5.13** *If  $f : A \rightarrow B$ ,  $U \in P_D A$ ,  $V \in P_D B$ ,  $f \in U \multimap V$  and  $Q \in V^\perp$ , then  $f ; Q \in U^\perp$ .*

**Proof** Similar to the proof of Lemma 5.12.  $\square$

**Notation** When  $s$  and  $t$  are traces of equal length, we will write  $s \text{ zip } t$  for the unique trace  $u$  such that  $\text{fst}^*(u) = s$  and  $\text{snd}^*(u) = t$ .

**Proposition 5.14**  *$D$  is a specification structure over  $\mathcal{S}\text{Proc}_{pr}$ .*

**Proof** The first requirement is that if  $A$  is any object of  $\mathcal{S}\text{Proc}_{pr}$  and  $U \in P_D A$ ,  $U\{\text{id}_A\}U$ . This follows from Lemma 5.11.

Next, suppose that  $A, B, C$  are objects of  $\mathcal{S}\text{Proc}_{pr}$  and  $U \in P_D A, V \in P_D B$  and  $W \in P_D C$ . If  $f : A \rightarrow B$  and  $g : B \rightarrow C$  with  $U\{f\}V$  and  $V\{g\}W$ , we need  $U\{f ; g\}W$ . Thus the goal is to prove that  $f ; g \perp \{P \otimes R \mid P \in U, R \in W^\perp\}$ .

Take  $P \in U$  and  $Q \in W^\perp$ . We need to prove that  $(f ; g) \perp (P \otimes Q)$ . Any common trace of  $f ; g$  and  $P \otimes Q$  arises from traces  $s$  and  $t$  such that  $f \xrightarrow{s}^* f', g \xrightarrow{t}^* g', P \xrightarrow{\text{fst}^*(s)}^* P', Q \xrightarrow{\text{snd}^*(t)}^* Q'$  and  $\text{snd}^*(s) = \text{fst}^*(t)$ . Then we have

$$f ; g \xrightarrow{\text{fst}^*(s) \text{ zip } \text{snd}^*(t)}^* f' ; g' \quad P \otimes Q \xrightarrow{\text{fst}^*(s) \text{ zip } \text{snd}^*(t)}^* P' \otimes Q'.$$

This gives  $P ; f \xrightarrow{\text{snd}^*(s)*} P' ; f'$  and  $g ; Q \xrightarrow{\text{fst}^*(t)*} g' ; Q'$ . By Lemmas 5.12 and 5.13,  $P ; f \in V$  and  $g ; R \in V^\perp$ . Hence  $(P ; f) \perp (g ; R)$ , so there is  $b$  such that  $P' ; f' \xrightarrow{b} P'' ; f''$  and  $g' ; Q' \xrightarrow{b} g'' ; Q''$ . By the definition of composition, there are  $a$  and  $c$  such that  $P' \xrightarrow{a} P''$ ,  $f' \xrightarrow{(a,b)} f''$ ,  $g' \xrightarrow{(b,c)} g''$  and  $Q' \xrightarrow{c} Q''$ . Hence  $f' ; g' \xrightarrow{(a,c)} f'' ; g''$  and  $P' \otimes Q' \xrightarrow{(a,c)} P'' \otimes Q''$ , as required.  $\square$

It is now legitimate to talk about the category  $\mathcal{S}Proc_D$  of deadlock-free processes. In order to prove that the  $*$ -autonomous structure of  $\mathcal{S}Proc$  lifts to  $\mathcal{S}Proc_D$ , we need to check the various conditions listed in Section 4. As an example of the style of proof required, we will verify one case.

**Proposition 5.15** *If  $U \in P_D A$  and  $V \in P_D B$ , then  $(U \otimes V)\{\text{symm}_{A,B}\}(V \otimes U)$ .*

**Proof** We need  $\text{symm} \in (U \otimes V) \multimap (V \otimes U)$ , i.e.  $\text{symm} \in ((U \otimes V) \otimes (V \otimes U)^\perp)^\perp$ , or equivalently  $\text{symm} \perp \{P \otimes Q \mid P \in U \otimes V, Q \in (V \otimes U)^\perp\}$ .

First, suppose that  $Q \in (V \otimes U)^\perp = \{R \otimes S \mid R \in V, S \in U\}^{\perp\perp\perp}$ , i.e.

$$Q \perp \{R \otimes S \mid R \in V, S \in U\}.$$

Defining  $Q' \stackrel{\text{def}}{=} Q[(b, a) \mapsto (a, b)]$ , it is clear that  $Q' \perp \{S \otimes R \mid S \in U, R \in V\}$  and so  $Q' \in (U \otimes V)^\perp$ .

Now suppose that  $P \in U \otimes V$  and  $Q \in (V \otimes U)^\perp$ , and  $s$  is a common trace of  $\text{symm}$  and  $P \otimes Q$ . The definition of  $\text{symm}$  means that  $\text{fst}^*(s) = \text{snd}^*(s)[(b, a) \mapsto (a, b)]$ . Also,  $\text{fst}^*(s)$  is a trace of  $P$  and  $\text{snd}^*(s)[(b, a) \mapsto (a, b)]$  is a trace of  $Q'$ . Because  $P \perp Q'$ , there is an action  $(a, b)$  available to both  $P$  and  $Q'$  after this trace. So  $Q$  can do  $(b, a)$  after the corresponding trace, and  $P \otimes Q$  can do  $((a, b), (b, a))$  after  $s$ . This action is also available to  $\text{symm}$  after  $s$ . Hence  $\text{symm} \perp P \otimes Q$ , as required.  $\square$

We also need to check that the products and coproducts lift to  $\mathcal{S}Proc_D$ . The necessary proofs will be postponed until Section 6, as they turn out to be more easily formulated in the language of ready specifications.

We will, however, prove that the UFPP lifts to  $\mathcal{S}Proc_D$ .

**Proposition 5.16** *Let  $A, B$  be objects of  $\mathcal{S}Proc_{pr}$ ,  $U \in P_D A$  and  $V \in P_D B$ . Let  $f : (A, U) \rightarrow (\circ A, \circ U)$  and  $g : (\circ B, \circ V) \rightarrow (B, V)$ , and let  $h : A \rightarrow B$  be the unique morphism in  $\mathcal{S}Proc$  satisfying  $h = f ; \circ h ; g$ . Then  $h : (A, U) \rightarrow (B, V)$  in  $\mathcal{S}Proc_D$ .*

**Proof** We need to prove that  $h \models U \multimap V$ , i.e. that  $h \perp \{P \otimes Q \mid P \in U, Q \in V^\perp\}$ . We will prove, by induction on the length of  $s$ , that

$$\forall s. \forall P \in U. \forall Q \in V^\perp. \left( \begin{array}{l} (P \otimes Q) \sqcap h \xrightarrow{s} (P' \otimes Q') \sqcap h' \\ \Rightarrow \exists (a, b). (P' \otimes Q') \sqcap h' \xrightarrow{(a,b)} (P'' \otimes Q'') \sqcap h'' \end{array} \right)$$

(Base case)  $s = \epsilon$ . Take  $P \in U$ ,  $Q \in V^\perp$ ,  $R \in \circ(U^\perp)$ ,  $S \in \circ V$ . Because  $f \perp P \otimes R$  there is  $a$  such that  $f \xrightarrow{(a,*)} f'$  and  $P \xrightarrow{a} P'$ . Because  $g \perp S \otimes Q$  there is  $b$  such that  $g \xrightarrow{(*,b)} g'$  and  $Q \xrightarrow{b} Q'$ . The transitions of  $f$  and  $g$  give  $h \xrightarrow{(a,b)} h'$ , and we also have  $P \otimes Q \xrightarrow{(a,b)} P' \otimes Q'$ .

(Inductive step) Assuming the result for traces of length  $n$ , consider traces of length  $n+1$ . Take  $P \in U$  and  $Q \in V^\perp$ . Because  $f \models U \multimap \circ U$  and  $g \models \circ V \multimap V$ , Lemmas 5.12 and 5.13 give  $P ; f \in \circ U$  and  $g ; Q \in \circ V$ . Suppose  $h \sqcap (P \otimes Q) \xrightarrow{s} h' \sqcap (P' \otimes Q')$ . This means

that there are traces  $u$  and  $v$  with  $f \xrightarrow{s \text{ zip } (*u)}^* f'$ ,  $o h \xrightarrow{(*u) \text{ zip } (*v)}^* k$  and  $g \xrightarrow{(*v) \text{ zip } t}^* g'$ . Also  $P \xrightarrow{s}^* P'$  and  $Q \xrightarrow{t}^* Q'$ . Writing  $P;f = o R$  and  $g;Q = o S$ , we have  $R \xrightarrow{u}^* R' = P';f'$  and  $S \xrightarrow{v}^* S' = g';Q'$ .

Now we have  $h \sqcap (R \otimes S) \xrightarrow{u \text{ zip } v}^* k \sqcap (R' \otimes S')$  with  $R \in U, S \in V^\perp$  and  $\text{length}((u \text{ zip } v)) = n$ . By the induction hypothesis, there is  $(c, d)$  such that  $k \sqcap (R \otimes S) \xrightarrow{(a,b)}^* k' \sqcap (R' \otimes S')$ . So there is  $(a, b)$  such that  $P' \xrightarrow{a} P'', f' \xrightarrow{(a,c)} f'', Q' \xrightarrow{b} Q''$  and  $g' \xrightarrow{(d,b)} g''$ . Hence  $h' \xrightarrow{(a,b)} f'';k'';g''$  and  $P' \otimes Q' \xrightarrow{(a,b)} P'' \otimes Q''$ .  $\square$

For later work it is useful to have a supply of properties over each type.

**Proposition 5.17** *For every object  $A$  of  $SProc_{pr}$ ,  $\{\max_A\}^\perp = \text{Proc}(A)$  and  $\text{Proc}(A)^\perp = \{\max_A\}$ .*

**Proof** For any  $P \in \text{Proc}(A)$ ,  $P \perp \max_A$ . Hence  $\text{Proc}(A) \perp \{\max_A\}$ . This means that  $\{\max_A\}^\perp \supseteq \text{Proc}(A)$ ; also,  $\{\max_A\}^\perp \subseteq \text{Proc}(A)$ . This gives  $\{\max_A\}^\perp = \text{Proc}(A)$ .

For the second part, we already have  $\{\max_A\} \subseteq \text{Proc}(A)^\perp$ . Now suppose that  $P \neq \max_A$ . There is a process  $P'$ , a trace  $s$  and an action  $a \in \Sigma_A$  such that  $sa \in S_A$  and  $P \xrightarrow{s}^* P'$  but  $P'$  cannot do  $a$ . Define  $Q$  to be the same process as  $P$ , except that the node  $P'$  is replaced by  $Q'$  where  $Q' = a : \max_{A/sa}$ . Then  $P \sqcap Q$  does not converge, so  $P \not\perp \text{Proc}(A)$ .  $\square$

**Corollary 5.18**  $\{\max_A\}^{\perp\perp} = \{\max_A\}$  and  $\text{Proc}(A)^{\perp\perp} = \text{Proc}(A)$ .

**Definition 5.19** *For each object  $A$  of  $SProc_{pr}$ , define two properties over  $A$ :  $\text{in}_A \stackrel{\text{def}}{=} \{\max_A\}$  and  $\text{out}_A \stackrel{\text{def}}{=} \text{Proc}(A)$ . Thus we have  $\text{in}_A^\perp = \text{out}_A$  and  $\text{out}_A^\perp = \text{in}_A$ .*

A port of type  $(A, \text{in}_A)$  represents an input because the possible behaviour is described by  $\max_A$  which is always prepared to engage in any action. A port of type  $(A, \text{out}_A)$  represents a possibly non-deterministic output; no information is available about its possible behaviour.

**Proposition 5.20** *For all objects  $A$ ,*

$$(s \in S_A \Rightarrow \exists! a \in \Sigma_A. sa \in S_A) \iff (\text{in}_A = \text{out}_A).$$

**Corollary 5.21**  $I_D = \text{in}_I = \text{out}_I = I_D^\perp$ .

There are a few useful results on combinations of  $\text{in}$  and  $\text{out}$  properties.

**Proposition 5.22** *For any objects  $A$  and  $B$  of  $SProc_{pr}$ ,*

1.  $\text{in}_A \otimes \text{in}_B = \text{in}_{A \otimes B}$
2.  $\text{out}_A \wp \text{out}_B = \text{out}_{A \wp B}$
3.  $\text{out}_A \otimes \text{out}_B = \text{out}_{A \otimes B}$
4.  $\text{in}_A \wp \text{in}_B = \text{in}_{A \wp B}$

**Proof**

1. Follows from the fact that  $\max_A \otimes \max_B = \max_{A \otimes B}$ .
2. Follows from 1 by duality.

3. Since  $M_A \otimes M_B = \{P \otimes Q \mid P \in M_A, Q \in M_B\}^{\perp\perp}$ , it is enough to prove

$$\{P \otimes Q \mid P \in M_A, Q \in M_B\}^{\perp} = \{\mathbf{max}_{A \otimes B}\}.$$

Clearly

$$\mathbf{max}_{A \otimes B} \perp \{P \otimes Q \mid P \in M_A, Q \in M_B\}.$$

Suppose that  $R \in \mathbf{Proc}(A \otimes B)$  and  $R \neq \mathbf{max}_{A \otimes B}$ . At some point in the tree of  $R$ , there is an action  $(a, b)$  which is unavailable. For simplicity, say that  $R$  cannot do  $(a, b)$ . Then if  $P = a : \mathbf{max}_{A/a}$  and  $Q = b : \mathbf{max}_{B/b}$ ,  $(P \otimes Q) \not\leq R$ .

4. Follows from 3 by duality.  $\square$

**Proposition 5.23** *If  $P : A_1 \wp \cdots \wp A_n$  in  $\mathcal{SProc}$ , the  $A_i$  are progressive and  $P \downarrow$ , then  $P : (A_1, \mathbf{out}_{A_1}) \wp \cdots \wp (A_n, \mathbf{out}_{A_n})$  in  $\mathcal{SProc}_D$ .*

**Proof** It is immediate that if  $P : A$  in  $\mathcal{SProc}$ ,  $A$  is progressive and  $P$  is deadlock-free, then  $P : (A, \mathbf{out}_A)$  in  $\mathcal{SProc}_D$ . By Proposition 5.22, the result can be obtained by applying this observation to the type  $A_1 \wp \cdots \wp A_n$ .  $\square$

### 5.3 Loss of Degeneracy

The degeneracies present in  $\mathcal{SProc}$  (coincidence of  $\otimes$  and  $\wp$ , coincidence of  $\&$  and  $\oplus$ ) do not appear in  $\mathcal{SProc}_D$ .

**Proposition 5.24** *Define  $\mathcal{SProc}$  objects  $A$  and  $B$  by  $\Sigma_A = \{a, b\}$ ,  $S_A = \Sigma_A^*$ ,  $\Sigma_B = \{c, d\}$ ,  $S_B = \Sigma_B^*$ . Then  $\mathbf{out}_A \otimes \mathbf{in}_B \neq \mathbf{out}_A \wp \mathbf{in}_B$ .*

**Proof** We have

$$\begin{aligned} \mathbf{out}_A \otimes \mathbf{in}_B &= \{P \otimes \mathbf{max}_B \mid P \in \mathbf{Proc}(A)\}^{\perp\perp} \\ \mathbf{out}_A \wp \mathbf{in}_B &= (\mathbf{in}_A \otimes \mathbf{out}_B)^{\perp} \\ &= \{\mathbf{max}_A \otimes Q \mid Q \in \mathbf{Proc}(B)\}^{\perp}. \end{aligned}$$

Defining processes  $X$  and  $Y$  of type  $A \otimes B$  by

$$\begin{aligned} X &= (b, c) : X + (a, d) : X \\ Y &= (a, c) : Y + (b, d) : Y \end{aligned}$$

it is easy to see

$$\begin{aligned} X &\in \{P \otimes \mathbf{max}_B \mid P \in \mathbf{Proc}(A)\}^{\perp} \\ Y &\in \{\mathbf{max}_A \otimes Q \mid Q \in \mathbf{Proc}(B)\}^{\perp}. \end{aligned}$$

But  $X \not\leq Y$ , which means that  $X \notin \{P \otimes \mathbf{max}_B \mid P \in \mathbf{Proc}(A)\}^{\perp\perp}$ .  $\square$

Loss of compact closure is to be expected, as in general the arbitrary formation of cycles can lead to deadlock. Later in the paper we will present ways of constructing cyclic processes in particular cases.

**Proposition 5.25** *Define  $\mathcal{SProc}$  objects  $A$  and  $B$  by  $\Sigma_A = \{a\}$ ,  $S_A = \Sigma_A^*$ ,  $\Sigma_B = \{b\}$ ,  $S_B = \Sigma_B^*$ . Then  $\mathbf{out}_A \oplus \mathbf{out}_B \neq \mathbf{out}_A \& \mathbf{out}_B$ .*



**Proof** We have

$$\begin{aligned}
\text{out}_A \oplus \text{out}_B &= (\{\max_A\} \cup \{\max_B\})^{\perp\perp} \\
&= \{\max_A, \max_B\}^{\perp\perp} \\
\text{out}_A \&\text{out}_B &= (\text{out}_A \oplus \text{out}_B)^\perp \\
&= \{\max_A, \max_B\}^\perp,
\end{aligned}$$

omitting  $\text{inl}$  and  $\text{inr}$  for clarity. Now,  $\{\max_A, \max_B\}^\perp = \{\max_A + \max_B\}$ , but

$$\{\max_A + \max_B\}^\perp \supseteq \{\max_A + \max_B, \max_A, \max_B\}$$

and so  $\text{out}_A \oplus \text{out}_B$  is strictly larger than  $\text{out}_A \&\text{out}_B$ .  $\square$

Although  $\otimes$  and  $\wp$  are distinct in  $\mathcal{S}Proc_D$ , the Mix rule is still valid.

**Proposition 5.26** *For any objects  $(A, U)$ ,  $(B, V)$  of  $\mathcal{S}Proc_D$ , we have  $\text{id}_{A \otimes B} : (A, U) \otimes (B, V) \rightarrow (A, U) \wp (B, V)$ .*

**Proof** By Lemma 5.11 it is enough to show that  $U \otimes V \subseteq U \wp V$ , i.e.

$$\{P \otimes Q \mid P \in U, Q \in V\}^{\perp\perp} \subseteq \{R \otimes S \mid R \in U^\perp, S \in V^\perp\}^\perp.$$

This follows from

$$\{R \otimes S \mid R \in U^\perp, S \in V^\perp\} \subseteq \{P \otimes Q \mid P \in U, Q \in V\}^\perp$$

which in turn follows from

$$\{R \otimes S \mid R \in U^\perp, S \in V^\perp\} \perp \{P \otimes Q \mid P \in U, Q \in V\}.$$

Take  $P \in U$ ,  $Q \in V$ ,  $R \in U^\perp$ ,  $S \in V^\perp$ . If  $(P \otimes Q) \sqcap (R \otimes S) \xrightarrow{s} (P' \otimes Q') \sqcap (R' \otimes S')$  then  $P \sqcap R \xrightarrow{\text{fst}^*(s)} P' \sqcap R'$  and  $Q \sqcap S \xrightarrow{\text{snd}^*(s)} Q' \sqcap S'$ . Because  $P \perp R$  and  $Q \perp S$  there are  $a, b$  such that  $P' \xrightarrow{a} P''$ ,  $R' \xrightarrow{a} R''$ ,  $Q' \xrightarrow{b} Q''$ ,  $S' \xrightarrow{b} S''$ . This implies  $(P' \otimes Q') \sqcap (R' \otimes S') \xrightarrow{(a,b)} (P'' \otimes Q'') \sqcap (R'' \otimes S'')$ , and so  $(P \otimes Q) \perp (R \otimes S)$ .  $\square$

## 6 Ready Specifications

The specification structure presented in the previous section results in a category which supports compositional verification of deadlock-freedom, but there is perhaps a lack of intuition about the use of sets of processes. There is an alternative specification structure for deadlock-freedom, which was in fact the first to be discovered, and which is more easily motivated. It turns out to be equivalent to the sets of processes approach.

### 6.1 Fundamentals

The reason why deadlock-freedom is not generally preserved by composition in  $\mathcal{S}Proc$  is that two deadlock-free processes may, when forced to communicate, reach states from which no further communication is possible even though both processes have more actions available. This observation leads to the idea that if a type is to guarantee compositional deadlock-freedom, it must specify something about which actions a process must be prepared to perform in certain states. The way in which this information is captured is via

the notions of ready pair and ready specification. We will see that ready specifications arise naturally as “datatypes extended in time”.

Consider a function  $f : A_1 \times \cdots \times A_n \rightarrow B$ , with each of the types  $A_1, \dots, A_n$  and  $B$  determining a set of values. Suppose that in one computation step  $f$  receives  $n$  inputs and simultaneously produces an output. In each of the  $n$  inputs,  $f$  is prepared to receive any value—indeed, it is precisely this property which characterises them as inputs—while in the output,  $f$  is free to choose which value appears. More generally, if  $f$  is a relation rather than a function, we can think of the  $A_i$  and  $B$  as the types of ports rather than inputs or outputs. In this case, the picture of which values may appear in each port is more complex—some input values may not be accepted, and some outputs may be non-deterministic. More generally still,  $f$  may be a process with dynamic behaviour extending through time. Its behaviour can still be characterised by the sets of values which may appear in each port, but now these sets depend on the state of the process. If we indicate the state of a process by the sequence of actions which led to that state, then the following definitions are very natural.

**Definition 6.1** *A ready pair over an SProc object  $A = (\Sigma_A, S_A)$  is a pair  $(s, X)$  in which  $s \in S_A$  and  $X \subseteq \Sigma_A$ , such that  $\forall x \in X. sx \in S_A$ . The set  $X$  is the ready set of the ready pair. A proper ready pair is a ready pair  $(s, X)$  with  $X \neq \emptyset$ . The set of proper ready pairs over an object  $A$  is denoted by  $\text{RP}(A)$ . If  $P$  is a process of type  $A$ , then*

$$\begin{aligned} \text{initials}(P) &\stackrel{\text{def}}{=} \{x \in \Sigma_A \mid \exists Q. P \xrightarrow{x} Q\} \\ \text{readies}(P) &\stackrel{\text{def}}{=} \{(s, X) \mid (P \xrightarrow{s}^* Q) \wedge (X = \text{initials}(Q))\}. \end{aligned}$$

For any process  $P$ ,  $\text{readies}(P)$  is the set of ready pairs  $(s, X)$  representing the actions (those in  $X$ ) in which  $P$  is ready to engage after performing a sequence  $s$  of actions. Note that  $\text{readies}(P)$  does not necessarily consist entirely of proper ready pairs.

**Definition 6.2** *A process  $P$  of type  $A$  is deadlock-free if and only if there is no trace  $s \in S_A$  such that  $P \xrightarrow{s}^* \text{nil}$ . Equivalently, if and only if there is no trace  $s \in S_A$  such that  $(s, \emptyset) \in \text{readies}(P)$ .*

This is equivalent to the definition of convergence used in Section 5. For example,

$$\text{readies}(a.b.\text{nil} + a.c.\text{nil}) = \{(\varepsilon, \{a\}), (a, \{b\}), (a, \{c\}), (ab, \emptyset), (ac, \emptyset)\}$$

and if  $P = a.P$ ,

$$\text{readies}(P) = \{(a^n, \{a\}) \mid n < \omega\}.$$

The idea of a ready pair, and the related notions of *failures* and *refusals*, appear in the process algebra literature [12, 16, 27]. There, however, they are used to define semantic alternatives to bisimulation; the use made of ready pairs in this paper is very different.

We use an orthogonality relation on ready pairs, rather than on processes as in Section 5.

**Definition 6.3** *The orthogonality relation  $\perp$  on  $\text{RP}(A)$  is defined by*

$$(s, X) \perp (t, Y) \stackrel{\text{def}}{=} ((s = t) \Rightarrow X \cap Y \neq \emptyset).$$

The idea is that if  $(s, X)$  and  $(t, Y)$  are ready pairs of two processes which are supposed to be communicating,  $(s, X) \perp (t, Y)$  means that if they have been communicating so far ( $s = t$ ) there is some action which they are both prepared to do next ( $X \cap Y \neq \emptyset$ ) and thus

continue the communication. Again, this is a natural generalisation of the requirement in functional programming that an output always be connected to an input. If there are two ports, in which the respective sets of actions  $X$  and  $Y$  can occur, then connecting them together only leads to correct communication if  $X \cap Y \neq \emptyset$ . Taking the varying states of the processes into account leads to the definition of orthogonality of ready pairs.

We lift the orthogonality relation to an operation of negation on sets of ready pairs.

**Definition 6.4** *Let  $\theta \subseteq \text{RP}(A)$  for some object  $A$ .*

$$\begin{aligned} (s, X) \perp \theta &\stackrel{\text{def}}{=} \forall (t, Y) \in \theta. (s, X) \perp (t, Y) \\ \theta^\perp &\stackrel{\text{def}}{=} \{(s, X) \in \text{RP}(A) \mid (s, X) \perp \theta\}. \end{aligned}$$

**Proposition 6.5** *For  $\theta, \varphi \subseteq \text{RP}(A)$ ,*

1.  $\theta \subseteq \varphi \Rightarrow \varphi^\perp \subseteq \theta^\perp$
2.  $\theta \subseteq \theta^{\perp\perp}$
3.  $\theta^{\perp\perp\perp} = \theta^\perp$ .

**Definition 6.6** *A ready specification over an object  $A$  is a non-empty set  $\theta$  of proper ready pairs over  $A$ , satisfying*

- $((s, X) \in \theta) \wedge (x \in X) \Rightarrow \exists Y. (sx, Y) \in \theta$
- $(sx, Y) \in \theta \Rightarrow \exists X. [(s, X) \in \theta \wedge x \in X]$ .

*The set of ready specifications over  $A$  is denoted by  $\text{RS}(A)$ .*

**Proposition 6.7** *If  $A$  is progressive then  $\text{RP}(A) \in \text{RS}(A)$ .*

**Proof** We need to check that  $\text{RP}(A)$  satisfies the closure conditions of Definition 6.6. For the first, suppose that  $(s, X) \in \text{RP}(A)$  and  $x \in X$ . Then  $sx \in S_A$  by the definition of ready pair. Since  $A$  is progressive, there is  $a \in \Sigma_A$  such that  $sxa \in S_A$ . This means that  $(sx, \{a\}) \in \text{RP}(A)$ .

For the second, observe that if  $(sx, Y) \in \text{RP}(A)$  then  $(s, \{x\}) \in \text{RP}(A)$  with  $x \in \{x\}$ .  $\square$

**Corollary 6.8** *If  $A$  is progressive then  $\text{RS}(A) \neq \emptyset$ .*

**Proposition 6.9** *For any object  $A$  of  $\text{SProc}_{pr}$ , the following hold.*

1.  $\text{RP}(A)^{\perp\perp} = \text{RP}(A)$
2.  $\text{RP}(A)^\perp = \{(s, \Sigma_A(s)) \mid s \in S_A\}$ .

**Proof**

1.  $\text{RP}(A)^{\perp\perp}$  is a set of ready pairs, so  $\text{RP}(A)^{\perp\perp} \subseteq \text{RP}(A)$ . Also  $\text{RP}(A) \subseteq \text{RP}(A)^{\perp\perp}$  by Proposition 6.5.
2. It is clear that  $\{(s, \Sigma_A(s)) \mid s \in S_A\} \perp \text{RP}(A)$ . Conversely, suppose that  $(s, X) \perp \text{RP}(A)$ . Because  $(s, \{x\}) \in \text{RP}(A)$  for every  $x \in \Sigma_A$  such that  $sx \in S_A$ , the definition of orthogonality means that  $x \in X$  for each such  $x$ . Hence  $X = \Sigma_A(s)$  as claimed.  $\square$

**Corollary 6.10** For any object  $A$  of  $\mathcal{SProc}_{pr}$ , and  $\theta \subseteq \mathbf{RP}(A)$ ,

$$\theta^\perp \supseteq \{(s, \Sigma_A(s)) \mid s \in S_A\}.$$

**Proof** If  $\theta \subseteq \mathbf{RP}(A)$  then  $\{(s, \Sigma_A(s)) \mid s \in S_A\} = \mathbf{RP}(A)^\perp \subseteq \theta^\perp$ .  $\square$

## 6.2 The Specification Structure $D'$

Again following the sequence of definitions in Section 4.1, we can define the specification structure  $D'$  over  $\mathcal{SProc}_{pr}$ .

**Definition 6.11**

1.  $P_{D'} A \stackrel{\text{def}}{=} \{\theta \in \mathbf{RS}(A) \mid \theta^{\perp\perp} = \theta\}$ .
2.  $P \models \theta \stackrel{\text{def}}{=} \text{readies}(P) \subseteq \theta$ .
3.  $(\Leftrightarrow)^\perp : P_{D'} A \rightarrow P_{D'} A$  has already been defined.
- 4.

$$\begin{aligned} \theta \wp \varphi &\stackrel{\text{def}}{=} \{(s, U \times V) \mid (\text{fst}^*(s), U) \in \theta^\perp, (\text{snd}^*(s), V) \in \varphi^\perp\}^\perp \\ \theta \otimes \varphi &\stackrel{\text{def}}{=} (\theta^\perp \wp \varphi^\perp)^\perp \\ \theta \multimap \varphi &\stackrel{\text{def}}{=} \theta^\perp \wp \varphi \\ I_{D'} &\stackrel{\text{def}}{=} \{(s, \{*\}) \mid s \in S_I\}. \end{aligned}$$

5.  $\theta \{f\} \varphi \stackrel{\text{def}}{=} f \models \theta \multimap \varphi$ .
6. The definitions required to lift the additive and temporal structure of  $\mathcal{SProc}$  to  $\mathcal{SProc}_{D'}$  are

$$\begin{aligned} \theta \&\varphi &\stackrel{\text{def}}{=} (\{(\text{inl}^*(s), \text{inl}(X)) \mid (s, X) \in \theta^\perp\} \cup \{(\text{inr}^*(t), \text{inr}(Y)) \mid (t, Y) \in \varphi^\perp\})^\perp \\ \theta \oplus \varphi &\stackrel{\text{def}}{=} (\theta^\perp \&\varphi^\perp)^\perp \\ \circ \theta &\stackrel{\text{def}}{=} (\{(\varepsilon, \{*\})\} \cup \{(*s, X) \mid (s, X) \in \theta^\perp\})^\perp \end{aligned}$$

**Proposition 6.12** If  $\theta \in P_{D'} A$  and  $P \models \theta$  then  $P$  is deadlock-free.

**Proof** Follows from the fact that  $\theta$  contains only proper ready pairs.  $\square$

**Proposition 6.13** If  $\theta \in P_{D'} A$  then  $\theta \supseteq \{(s, \Sigma_A(s)) \mid s \in S_A\}$ .

**Proof** Follows from Corollary 6.10, because  $\theta = (\theta^\perp)^\perp$ .  $\square$

We will now prove that there is an isomorphism between properties in  $D'$  and properties in  $D$ . This will enable us to deduce that  $D'$  is a specification structure, and that  $\mathcal{SProc}_{D'}$  and  $\mathcal{SProc}_D$  are isomorphic.

### 6.3 Equivalence of $D$ and $D'$

We will now prove that for each object  $A$  of  $\mathcal{S}Proc_{pr}$  there is a bijection between  $P_DA$  and  $P_{D'}A$ , and this bijection preserves all the operations on properties exactly. Also, satisfaction of properties is preserved. It turns out that this leads to an *isomorphism* between  $\mathcal{S}Proc_D$  and  $\mathcal{S}Proc_{D'}$ .

We now have two versions of orthogonality and of every operation on properties. To avoid confusion we will stick to the convention of using  $\theta, \varphi, \dots$  for ready specifications and  $U, V, \dots$  for sets of processes, and begin by proving that the two notions of orthogonality are compatible.

**Lemma 6.14** *If  $P, Q \in \mathbf{Proc}(A)$ , then  $P \perp Q \iff \text{readies}(P) \perp \text{readies}(Q)$ .*

**Proof** Suppose  $P \perp Q$ ,  $(s, X) \in \text{readies}(P)$  and  $(s, Y) \in \text{readies}(Q)$ . So  $P \xrightarrow{s}^* P'$  and  $Q \xrightarrow{s}^* Q'$ , and orthogonality of  $P$  and  $Q$  implies that there is an action  $a$  such that  $P' \xrightarrow{a} P''$  and  $Q' \xrightarrow{a} Q''$ . This means that  $a \in \text{initials}(P') = X$  and  $a \in \text{initials}(Q') = Y$ , so  $(s, X) \perp (s, Y)$ .

Conversely suppose  $\text{readies}(P) \perp \text{readies}(Q)$ ,  $P \xrightarrow{s}^* P'$  and  $Q \xrightarrow{s}^* Q'$ . Then we have  $(s, \text{initials}(P')) \in \text{readies}(P)$  and  $(s, \text{initials}(Q')) \in \text{readies}(Q)$ , and this implies

$$\text{initials}(P') \cap \text{initials}(Q') \neq \emptyset.$$

Thus there is an action  $a$  such that  $P' \xrightarrow{a} P''$  and  $Q' \xrightarrow{a} Q''$ , so  $P \perp Q$ . □

**Definition 6.15** *Let  $U \in P_DA$  and  $\theta \in P_{D'}A$ .*

$$\begin{aligned} F(\theta) &\stackrel{\text{def}}{=} \{P \in \mathbf{Proc}(A) \mid \text{readies}(P) \subseteq \theta\} \\ G(U) &\stackrel{\text{def}}{=} \bigcup \{\text{readies}(P) \mid P \in U\}. \end{aligned}$$

**Proposition 6.16** *If  $\theta \in P_{D'}A$  then  $GF(\theta) = \theta$ .*

**Proof** If  $(s, X) \in GF(\theta)$  there is  $P \in F(\theta)$  with  $(s, X) \in \text{readies}(P)$ . This means that  $(s, X) \in \theta$ , because  $P \in F(\theta) \Rightarrow \text{readies}(P) \subseteq \theta$ . Hence  $GF(\theta) \subseteq \theta$ .

If  $(s, X) \in \theta$  then establishing  $(s, X) \in GF(\theta)$  requires  $P \in F(\theta)$  such that  $(s, X) \in \text{readies}(P)$ . This means finding  $P$  with  $\text{readies}(P) \subseteq \theta$  and  $(s, X) \in \text{readies}(P)$ . We know that  $(t, \Sigma_A(t)) \in \theta$  for any trace  $t \in S_A$ . In  $\max_A$  there is a unique state reachable by the trace  $s$ . By removing branches from this state, we can construct a process  $P$  with the required property. □

**Proposition 6.17** *If  $U \in P_DA$  then  $FG(U) = U$ .*

**Proof** If  $P \in U$  then  $\text{readies}(P) \subseteq G(U)$ , so  $P \in FG(U)$ . Hence  $U \subseteq FG(U)$ .

If  $P \in FG(U)$  then  $\text{readies}(P) \subseteq G(U)$ . So for any  $(s, X) \in \text{readies}(P)$  there is  $Q \in U$  such that  $(s, X) \in \text{readies}(Q)$ . If  $R \in U^\perp$  and  $(t, Y) \in \text{readies}(R)$ , this means that  $(s, X) \perp (t, Y)$ , because  $R \perp Q$  and by Lemma 6.14. Hence  $P \perp R$ , i.e.  $P \in U^{\perp\perp} = U$ . Thus  $FG(U) \subseteq U$ . □

**Lemma 6.18** *Satisfaction is preserved by the correspondence between  $D$  and  $D'$ , i.e.*

$$\begin{aligned} \text{readies}(P) \subseteq \theta &\iff P \in F(\theta) \\ P \in U &\iff \text{readies}(P) \subseteq G(U). \end{aligned}$$

**Proof** The only non-trivial part is  $\text{readies}(P) \subseteq G(U) \Rightarrow P \in U$ ; the others follow easily from the definitions of  $F$  and  $G$ . If  $\text{readies}(P) \subseteq G(U)$  then  $P \in FG(U) = U$ .  $\square$

**Lemma 6.19**  $F(\theta^\perp) \subseteq F(\theta)^\perp$  and  $G(U^\perp) \subseteq G(U)^\perp$ .

**Proof** If  $P \in F(\theta^\perp)$  and  $Q \in F(\theta)$  then  $\text{readies}(P) \subseteq \theta^\perp$  and  $\text{readies}(Q) \subseteq \theta$ , so  $P \perp Q$ . If  $(s, X) \in G(U^\perp)$  and  $(t, Y) \in G(U)$  then  $\exists P \in U^\perp. (s, X) \in \text{readies}(P)$  and  $\exists Q \in U. (t, Y) \in \text{readies}(Q)$ . Since  $P \perp Q$ ,  $\text{readies}(P) \perp \text{readies}(Q)$  and hence  $(s, X) \perp (t, Y)$ .  $\square$

**Lemma 6.20** *If  $\theta \in P_{D'}A$ , there are processes  $P_1, \dots, P_n$  such that*

$$\text{readies}(P_1) \cup \dots \cup \text{readies}(P_n) = \theta.$$

**Proof** Define a labelled transition system whose states are the ready pairs in  $\theta$ , with transitions defined by

$$\frac{a \in X}{(s, X) \xrightarrow{a} (sa, Y)}.$$

We have  $(s, \Sigma_A(s)) \in \theta$  for each  $s \in S_A$ . So for any pair  $(sa, Y) \in \theta$  there is the transition  $(s, \Sigma_A(s)) \xrightarrow{a} (sa, Y)$ , which means that every state is reachable from  $(\varepsilon, \Sigma_A(\varepsilon))$ , except for any  $(\varepsilon, X)$  with  $X \neq \Sigma_A(\varepsilon)$ . This means that the states  $(\varepsilon, X)$  can be taken as the processes  $P_1, \dots, P_n$ .  $\square$

**Proposition 6.21**  $F(\theta^\perp) = F(\theta)^\perp$ .

**Proof** It is enough to prove  $F(\theta)^\perp \subseteq F(\theta^\perp)$ . If  $P \in F(\theta)^\perp$  then  $P \perp F(\theta)$ . Let  $Q_1, \dots, Q_n$  be such that  $\text{readies}(Q_1) \cup \dots \cup \text{readies}(Q_n) = \theta$ . Each  $Q_i \in F(\theta)$ , hence  $P \perp Q_i$  for each  $i$ . So  $\text{readies}(P) \perp \theta$  and hence  $\text{readies}(P) \subseteq \theta^\perp$ . Thus  $P \in F(\theta^\perp)$ .  $\square$

**Proposition 6.22**  $G(U^\perp) = G(U)^\perp$ .

**Proof**

$$\begin{aligned} G(U)^\perp &= G(F(G(U)^\perp)) \\ &= G(F(G(U))^\perp) \\ &= G(U^\perp). \end{aligned}$$

$\square$

**Corollary 6.23**  $F(\theta) = F(\theta)^{\perp\perp}$  and  $G(U) = G(U)^{\perp\perp}$ .

**Proposition 6.24** *If  $\theta \in P_{D'}A$  and  $\varphi \in P_{D'}B$  then  $F(\theta \wp \varphi) = F(\theta) \wp F(\varphi)$ .*

**Proof** It is enough to show that  $F(\theta^\perp \wp \varphi^\perp) = F(\theta^\perp) \wp F(\varphi^\perp)$ . Now,

$$\theta^\perp \wp \varphi^\perp = \{(s, A \times B) \mid (\text{fst}^*(s), A) \in \theta, (\text{snd}^*(s), B) \in \varphi\}^\perp$$

and

$$\begin{aligned}
F(\theta^\perp) \wp F(\varphi^\perp) &= F(\theta)^\perp \wp F(\varphi)^\perp \\
&= (F(\theta) \otimes F(\varphi))^\perp \\
&= \{P \otimes Q \mid P \in F(\theta), Q \in F(\varphi)\}^\perp
\end{aligned}$$

so we need to show that

$$\begin{aligned}
F[\{(s, A \times B) \mid (\text{fst}^*(s), A) \in \theta, (\text{snd}^*(s), B) \in \varphi\}^\perp] = \\
\{P \otimes Q \mid P \in F(\theta), Q \in F(\varphi)\}^\perp.
\end{aligned}$$

We will consider the two inclusions separately. If

$$R \in F[\{(s, A \times B) \mid (\text{fst}^*(s), A) \in \theta, (\text{snd}^*(s), B) \in \varphi\}^\perp]$$

then

$$\text{readies}(R) \perp \{(s, A \times B) \mid (\text{fst}^*(s), A) \in \theta, (\text{snd}^*(s), B) \in \varphi\}.$$

For any  $s, A, B$  with  $(\text{fst}^*(s), A) \in \theta$  and  $(\text{snd}^*(s), B) \in \varphi$ ,  $(s, X) \in \text{readies}(R)$  implies that there is  $(a, b) \in X$  with  $a \in A$  and  $b \in B$ . So if  $\text{readies}(P) \subseteq \theta$  and  $\text{readies}(Q) \subseteq \varphi$ ,  $R \perp (P \otimes Q)$ .

For the other inclusion, suppose that  $R \perp \{P \otimes Q \mid P \in F(\theta), Q \in F(\varphi)\}$ . We need to show that  $\text{readies}(R) \perp \{(s, A \times B) \mid (\text{fst}^*(s), A) \in \theta, (\text{snd}^*(s), B) \in \varphi\}$ . Let  $(s, X) \in \text{readies}(R)$ ,  $(\text{fst}^*(s), A) \in \theta$  and  $(\text{snd}^*(s), B) \in \varphi$ . By Lemma 6.20 there are  $P$  and  $Q$  such that  $(\text{fst}^*(s), A) \in \text{readies}(P)$ ,  $(\text{snd}^*(s), B) \in \text{readies}(Q)$ ,  $P \in F(\theta)$  and  $Q \in F(\varphi)$ . Because  $R \perp P \otimes Q$ , after the trace  $s$  there is an action  $(a, b)$  available to both  $R$  and  $P \otimes Q$ . Hence  $(a, b) \in X \cap (A \times B)$ .  $\square$

**Corollary 6.25**  $F(\theta \otimes \varphi) = F(\theta) \otimes F(\varphi)$ .

**Proof** This follows from the fact that  $F$  preserves  $\wp$  and  $(\Leftrightarrow)^\perp$ , and duality of  $\otimes$  and  $\wp$ .  $\square$

**Corollary 6.26**  $G(U \wp V) = G(U) \wp G(V)$  and  $G(U \otimes V) = G(U) \otimes G(V)$ .

**Proof**

$$\begin{aligned}
G(U \wp V) &= G(FG(U) \wp FG(V)) \\
&= GF(G(U) \wp G(V)) \\
&= G(U) \wp G(V).
\end{aligned}$$

Again,  $G(U \otimes V) = G(U) \otimes G(V)$  follows easily.  $\square$

**Proposition 6.27**  $F(\theta \& \varphi) = F(\theta) \& F(\varphi)$  and  $G(U \& V) = G(U) \& G(V)$ .

**Proof**

$$\begin{aligned}
F(\theta \& \varphi) &= F((\theta \& \varphi)^{\perp\perp}) \\
&= (F(\theta \& \varphi))^{\perp\perp} \\
&= \{P \mid P \models \theta \& \varphi\}^{\perp\perp} \\
&= \{Q[\text{inl}] + R[\text{inr}] \mid Q \models \theta, R \models \varphi\}^{\perp\perp} \\
&= \{Q[\text{inl}] + R[\text{inr}] \mid Q \in F(\theta), R \in F(\varphi)\}^{\perp\perp}
\end{aligned}$$

$$= F(\theta) \& F(\varphi).$$

$$\begin{aligned} G(U \& V) &= G(F(G(U)) \& F(G(V))) \\ &= G(F(G(U) \& G(V))) \\ &= G(U) \& G(V). \end{aligned}$$

□

**Proposition 6.28**  $F(\circ\theta) = \circ F(\theta)$  and  $G(\circ U) = \circ G(U)$ .

**Proof**

$$\begin{aligned} F(\circ\theta) &= \{P \mid P \models \circ\theta\} \\ &= \{\circ Q \mid Q \models \theta\} \\ &= \circ F(\theta). \end{aligned}$$

$$\begin{aligned} G(\circ U) &= G(\circ F(G(U))) \\ &= G(F(\circ G(U))) \\ &= \circ G(U). \end{aligned}$$

□

**Proposition 6.29**  $F(\text{RP}(A)) = \text{out}_A$  and  $F(\text{RP}(A)^\perp) = \text{in}_A$ .

**Proof** It is sufficient to prove the first statement.

$$\begin{aligned} F(\text{RP}(A)) &= \{P \in \text{Proc}(A) \mid \text{readies}(P) \subseteq \text{RP}(A)\} \\ &= \text{Proc}(A) \\ &= \text{out}_A. \end{aligned}$$

□

From the definitions of  $\text{RP}(A)$  and  $\text{RP}(A)^\perp$ , we have that in the ready specifications formulation,  $\text{in}_A = \{(s, \Sigma_A(s)) \mid s \in S_A\}$  and  $\text{out}_A = \{(s, X) \mid s \in S_A, \emptyset \neq X \subseteq \Sigma_A(s)\}$ . This provides an alternative view of why the properties **in** and **out** correspond to input and output. A port of type **in** is always ready to receive any action in the available alphabet, whereas a port of type **out** can enter states in which arbitrary subsets of the alphabet are not available.

## 6.4 Products and Coproducts

Now that the specification structures  $D$  and  $D'$  have been shown to be equivalent, any calculations relating to deadlock-freedom can be carried out in whichever setting is more convenient. The proof that products and coproducts lift to  $\mathcal{S}\text{Proc}_D$  was omitted from Section 5; we will now present it in terms of ready specifications, which turns out to be easier. By duality, it is sufficient to consider products.

**Lemma 6.30** *Let  $A, B$  be objects of  $\mathcal{S}\text{Proc}_{pr}$  with  $\theta \in P_{D'}A$ ,  $\varphi \in P_{D'}B$ .*

1. *If  $s \neq \varepsilon$  then  $(\text{inl}^*(s), X) \in \theta \& \varphi \iff (s, \{x \mid \text{inl}(x) \in X\}) \in \theta$ .*



2. If  $s \neq \varepsilon$  then  $(\text{inr}^*(s), X) \in \theta \ \& \ \varphi \iff (s, \{x \mid \text{inr}(x) \in X\}) \in \varphi$ .
3.  $(\varepsilon, X) \in \theta \ \& \ \varphi \iff \exists U, V. (\varepsilon, U) \in \theta, (\varepsilon, V) \in \varphi$  and  $X = \text{inl}(U) \cup \text{inr}(V)$ .

**Proof**

1.  $(\Rightarrow)$  We will show that  $(s, \{x \mid \text{inl}(x) \in X\}) \perp (t, Y)$  for every  $(t, Y) \in \theta^\perp$ . It is sufficient to consider  $(s, Y) \in \theta^\perp$ ; we then need  $\{x \mid \text{inl}(x) \in X\} \cap Y \neq \emptyset$ . Since  $(\text{inl}^*(s), X) \in \theta \ \& \ \varphi$ , the definition of  $\theta \ \& \ \varphi$  implies that  $X \cap \text{inl}(Y) \neq \emptyset$ , and we are done.

$(\Leftarrow)$  We need  $(\text{inl}^*(s), X) \perp (\text{inl}^*(s), \text{inl}(U))$  for every  $(s, U) \in \theta^\perp$ . Since

$$(s, \{x \mid \text{inl}(x) \in X\}) \in \theta$$

we have  $U \cap \{x \mid \text{inl}(x) \in X\} \neq \emptyset$ , and so  $X \cap \text{inl}(U) \neq \emptyset$ .

2. An identical argument.

3.  $(\Rightarrow)$  Take  $U \stackrel{\text{def}}{=} \{x \mid \text{inl}(x) \in X\}$ ,  $V \stackrel{\text{def}}{=} \{x \mid \text{inr}(x) \in X\}$ , so that  $X = \text{inl}(U) \cup \text{inr}(V)$ . To show that  $(\varepsilon, U) \in \theta$ , consider  $(\varepsilon, Y) \in \theta^\perp$ . The definition of  $\theta \ \& \ \varphi$  implies  $(\varepsilon, X) \perp (\varepsilon, \text{inl}(Y))$  and so  $X \cap \text{inl}(Y) \neq \emptyset$ . Hence  $U \cap Y \neq \emptyset$ , i.e.  $(\varepsilon, U) \perp (\varepsilon, Y)$ . So  $(\varepsilon, U) \in \theta^{\perp\perp} = \theta$ .

An identical argument shows that  $(\varepsilon, V) \in \varphi$ .

$(\Leftarrow)$  Suppose  $X = \text{inl}(U) \cup \text{inr}(V)$  with  $(\varepsilon, U) \in \theta$  and  $(\varepsilon, V) \in \varphi$ . For any  $(\varepsilon, W) \in \theta^\perp$ ,  $W \cap U \neq \emptyset$  and hence  $W \cap X \neq \emptyset$ . For any  $(\varepsilon, Z) \in \varphi^\perp$ ,  $Z \cap V \neq \emptyset$  and hence  $X \cap V \neq \emptyset$ . Thus  $(\varepsilon, X) \in \theta \ \& \ \varphi$ .  $\square$

**Proposition 6.31** *Let  $A, B, C$  be objects of  $\mathcal{SProc}_{pr}$  and let  $\theta, \varphi, \psi \in P_{D'}A, P_{D'}B, P_{D'}C$ . Let  $f : A \rightarrow B$  and  $g : A \rightarrow C$  with  $\theta\{f\}\varphi$  and  $\theta\{g\}\psi$ .*

1.  $(\theta \ \& \ \varphi)\{\pi_1\}\theta$
2.  $(\theta \ \& \ \varphi)\{\pi_2\}\varphi$
3.  $\theta\{\{f, g\}\}(\varphi \ \& \ \psi)$

**Proof**

1. We need

$$\begin{aligned} \text{readies}(\pi_1) &\subseteq (\theta \ \& \ \varphi) \multimap \theta \\ &= (\theta \ \& \ \varphi)^\perp \wp \theta \\ &= \{(s, U \times V) \mid (\text{fst}^*(s), U) \in \theta \ \& \ \varphi, (\text{snd}^*(s), V) \in \theta^\perp\}^\perp. \end{aligned}$$

Consider any  $s, U, V, X$  with

$$\begin{aligned} (\text{fst}^*(s), U) &\in \theta \ \& \ \varphi \\ (\text{snd}^*(s), V) &\in \theta^\perp \\ (s, X) &\in \text{readies}(\pi_1). \end{aligned}$$

For some  $(t \ \text{zip} \ t, Y) \in \text{readies}(\text{id}_A)$  we have  $s = \text{inl}^*(t) \ \text{zip} \ t$  and

$$X = \{(\text{inl}(a), b) \mid (a, b) \in Y\}.$$

So  $\text{fst}^*(s) = \text{inl}^*(t)$  and  $\text{snd}^*(s) = t$ .

By Lemma 6.30, either (1)  $\text{fst}^*(s) = \varepsilon$  and  $U = \text{inl}(W) \cup \text{inr}(W')$  for some  $W, W'$  with  $(\varepsilon, W) \in \theta$  and  $(\varepsilon, W') \in \varphi$ , in which case we will say  $(t, W) \in \theta$  with  $t = \varepsilon$ ; or (2)  $\text{fst}^*(s) \neq \varepsilon$  and  $U = \text{inl}(W)$  for some  $W$  with  $(t, W) \in \theta$ .

Because  $\text{id}_A \models \theta^\perp \wp \theta$ , i.e.

$$\text{readies}(\text{id}_A) \perp \{(u, Z \times T) \mid (\text{fst}^*(u), Z) \in \theta, (\text{snd}^*(u), T) \in \theta^\perp\},$$

in either case we have  $(t \text{ zip } t, Y) \perp (t \text{ zip } \text{snd}^*(s), W \times V)$ , so  $Y \cap (W \times V) \neq \emptyset$ . Hence  $X \cap (U \times V) \neq \emptyset$ , since  $X$  and  $U$  are defined by relabelling  $Y$  and  $W$ .

2. A symmetrical argument.

3. We need

$$\text{readies}(\langle f, g \rangle) \perp \{(s, U \times V) \mid (\text{fst}^*(s), U) \in \theta, (\text{snd}^*(s), V) \in (\varphi \& \psi)^\perp\}.$$

Consider any  $s, U, V, X$  with

$$\begin{aligned} (s, X) &\in \text{readies}(\langle f, g \rangle) \\ (\text{fst}^*(s), U) &\in \theta \\ (\text{snd}^*(s), V) &\in (\varphi \& \psi)^\perp. \end{aligned}$$

There are three cases.

- (a)  $s = \varepsilon$ , so that  $X = \text{initials}(f)[(a, b) \mapsto (a, \text{inl}(b))] \cup \text{initials}(g)[(a, c) \mapsto (a, \text{inr}(c))]$  and, by Lemma 6.30,  $V = \text{inl}(V_1) \cup \text{inr}(V_2)$  with  $(\varepsilon, V_1) \in \varphi^\perp$  and  $(\varepsilon, V_2) \in \psi^\perp$ . Then  $f \models \theta^\perp \wp \varphi$  implies that  $\text{initials}(f) \cap (U \times V_1) \neq \emptyset$ , and hence  $X \cap (U \times V) \neq \emptyset$ .
- (b)  $s \neq \varepsilon$  and  $\text{snd}^*(s) = \text{inl}^*(t)$ , so that  $X = Y[(a, b) \mapsto (a, \text{inl}(b))]$  with

$$(\text{fst}^*(s) \text{ zip } t, Y) \in \text{readies}(f)$$

and  $V = \text{inl}(V_1)$  with  $(t, V_1) \in \varphi^\perp$ . Then  $f \models \theta^\perp \wp \varphi$  implies that  $(\text{fst}^*(s) \text{ zip } t, Y) \perp (\text{fst}^*(s) \text{ zip } t, U \times V_1)$ ; so  $Y \cap (U \times V_1) \neq \emptyset$ , and hence  $X \cap (U \times V) \neq \emptyset$ .

- (c) A symmetrical case. □

## 6.5 Ready Equivalence and ${}^{\perp\perp}$ -invariance

It is possible that  $\text{readies}(P) = \text{readies}(Q)$  with  $P \neq Q$ , and in this case the processes  $P$  and  $Q$  satisfy exactly the same ready specifications. It is not possible for distinct processes to be contained in exactly the same sets of processes: if  $P \neq Q$  then  $P \notin \{Q\}$ . So it appears possible that sets of processes may make finer distinctions than ready specifications. However, if distinct processes have the same **readies**, they must be in the same  ${}^{\perp\perp}$ -invariant sets of processes, as shown by the following proposition.

**Proposition 6.32** *If  $P \in U$  and  $\text{readies}(P) = \text{readies}(Q)$  then  $Q \in U^{\perp\perp}$ .*

**Proof** If  $R \in U^\perp$  then  $\text{readies}(R) \perp \text{readies}(P)$ . So  $\text{readies}(R) \perp \text{readies}(Q)$ , which means that  $Q \in U^{\perp\perp}$ . □

Defining two processes to be *ready-equivalent* if they have the same **readies**, this result says that a  $\perp\perp$ -invariant set of processes must be the union of a collection of ready-equivalence classes. So membership of  $\perp\perp$ -invariant sets cannot distinguish processes more finely than ready-equivalence.

## 6.6 $\mathcal{S}Proc_D$ and $\mathcal{S}Proc_{D'}$ are Isomorphic

Because of the extremely strong connection which we have established between the specification structures  $D$  and  $D'$ , it turns out that the corresponding categories are *isomorphic*, i.e. that there are functors  $\mathcal{F} : \mathcal{S}Proc_D \leftrightarrow \mathcal{S}Proc_{D'} : \mathcal{G}$  with  $\mathcal{F}\mathcal{G} = \mathcal{I}_{\mathcal{S}Proc_{D'}}$  and  $\mathcal{G}\mathcal{F} = \mathcal{I}_{\mathcal{S}Proc_D}$ .

Given an object  $(A, U)$  of  $\mathcal{S}Proc_D$ ,  $\mathcal{F}(A, U) \stackrel{\text{def}}{=} (X, F(U))$ . Given a morphism  $f : (A, U) \rightarrow (B, V)$  of  $\mathcal{S}Proc_D$ ,  $\mathcal{F}(f) \stackrel{\text{def}}{=} f : (A, F(U)) \rightarrow (B, F(V))$ . Note that if  $f : (A, U) \rightarrow (B, V)$  then we have  $f \models U \multimap V$  and, because of the equivalence of satisfaction in  $D$  and  $D'$  and the fact that  $F$  preserves the linear connectives, this gives  $f \models F(U) \multimap F(V)$  also. Hence  $f$  really is a morphism  $(A, F(U)) \rightarrow (B, F(V))$  in  $\mathcal{S}Proc_{D'}$ . Because  $\mathcal{F}$  does not change morphisms, composition and identities are trivially preserved. The functor  $\mathcal{G}$  is defined similarly, and the fact that  $\mathcal{F}$  and  $\mathcal{G}$  are mutually inverse follows from the fact that  $F$  and  $G$  are mutually inverse. Furthermore,  $\mathcal{F}$  and  $\mathcal{G}$  preserve all of the structure of the categories; again, this is simply because  $F$  and  $G$  preserve all the structure.

## 7 Synchronous Networks

In this section we will consider some applications of our techniques to systems of practical interest. There is a class of concurrent systems to which our theory is very well suited; we call these systems *synchronous networks*. A synchronous network consists of a number of processes or nodes, each with various ports, which are connected together in some configuration. The key points are that once the network has been constructed, its topology does not change; and that the entire system is subject to the synchrony hypothesis with which we have been working throughout. The two main examples of synchronous networks are synchronous dataflow programs, written in languages such as SIGNAL [25] and LUSTRE [26], and systolic algorithms [20].

Given that the topology of a network never changes, the operation of categorical composition (parallel composition + hiding) is suitable for forming a fixed, private connection between two nodes. As we have already seen in Section 3 the structure of a compact closed category such as  $\mathcal{S}Proc$  allows arbitrary networks to be constructed by means of categorical operations. We are also interested in constructing networks in  $\mathcal{S}Proc_D$ , to ensure deadlock-freedom; however, loss of compact closure means that cyclic networks cannot be constructed without some additional analysis. By suitable use of the deadlock-free types **in** and **out**, and their properties, we are able to identify which cycles can always be safely constructed, and formulate an additional proof rule for those which may be unsafe.

### 7.1 Networks in a Compact Closed Category

Suppose we are working in a compact closed category, potentially one in which  $(\Leftrightarrow)^\perp$  is non-trivial. Suppose also that for each datatype used by a particular network, there is an object in the category suitable for modelling a port of that type. The  $(\Leftrightarrow)^\perp$  operation is used to switch between input and output, in the sense that a port of type  $A$  must be

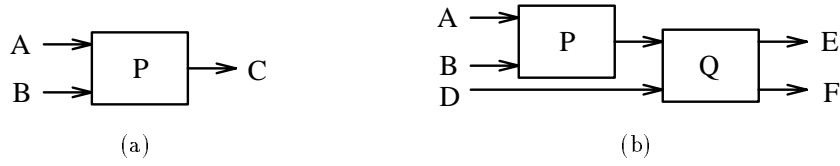


Figure 4: Two simple networks

connected to a port of type  $A^\perp$ , but at this stage we have not chosen which of  $A$  and  $A^\perp$  is input and which is output.

In general, when working with a  $*$ -autonomous category, a node with  $n$  ports of types  $A_1, \dots, A_n$  is represented by a process of type  $A_1 \wp \dots \wp A_n$ , i.e. a morphism  $P : I \rightarrow A_1 \wp \dots \wp A_n$ . The closed structure allows types to be moved across the arrow; in a compact closed category we do not have to worry about the effect that this has on the connectives, and we can replace every connective by  $\otimes$ . The only condition is that when a type is moved across the arrow,  $(\Leftrightarrow)^\perp$  must be applied. For example, a process  $P$  with three ports of types  $A^\perp$ ,  $B^\perp$  and  $C$  could be represented as  $P : C^\perp \rightarrow A^\perp \otimes B^\perp$ ,  $P : A \otimes C^\perp \rightarrow B^\perp$ ,  $P : A \otimes B \rightarrow C$ , and so on. If we wish to interpret  $A^\perp$  and  $B^\perp$  as input types and  $C$  as an output type, then the last of these makes the most sense, and we might draw the process as in Figure 4(a). In this way, any desired network can be constructed as a morphism in the category, with the calculation described in Section 3 being used to form cycles. For example, the morphism corresponding to the network in Figure 4(b) is

$$(P \otimes \text{id}_D) ; Q : A \otimes B \otimes D \rightarrow E \otimes F$$

where the morphisms corresponding to the individual nodes are  $P : A \otimes B \rightarrow C$  and  $Q : C \otimes D \rightarrow E \otimes F$ .

## 7.2 Deadlock-Free Types

We will now consider ways of typing the nodes of a network in  $\mathcal{S}Proc_D$ . In many cases it is possible to identify each port of a process as either an input or an output, and this allows us to use the types **in** and **out**. Since  $\mathcal{S}Proc$  is based on synchronization rather than value-passing, we need to define what it means for ports of an  $\mathcal{S}Proc$  process to be inputs.

**Definition 7.1** *Let  $P : A_1 \wp \dots \wp A_n$  in  $\mathcal{S}Proc$  and let  $J \subseteq \{1, \dots, n\}$ .  $P$  is receptive in the ports  $J$  if whenever  $P \xrightarrow{s} *Q$  and  $\forall j \in J. a_j \in \Sigma_{A_j}(\pi_j^*(s))$  then for each  $i \in \{1, \dots, n\} \Leftrightarrow J$  there is  $a_i \in \Sigma_{A_i}(\pi_i^*(s))$  such that  $Q \xrightarrow{(a_1, \dots, a_n)} R$  for some  $R$ .*

Receptivity in a set of ports means those ports correspond to inputs and are independently able to receive arbitrary values. When a dataflow node is modelled by an  $\mathcal{S}Proc$  process, that process is receptive in each port which we consider to be an input of the node.

**Proposition 7.2** *Let  $P : A_1 \wp \dots \wp A_n$  be any  $\mathcal{S}Proc$  process and let  $J$  be the set of ports in which it is receptive. Defining  $\theta_i \in P_D A_i$  by*

$$\theta_i \stackrel{\text{def}}{=} \begin{cases} \text{in} & \text{if } i \in J \\ \text{out} & \text{otherwise} \end{cases}$$

*gives  $P : (A_1, \theta_1) \wp \dots \wp (A_n, \theta_n)$  in  $\mathcal{S}Proc_D$ .*

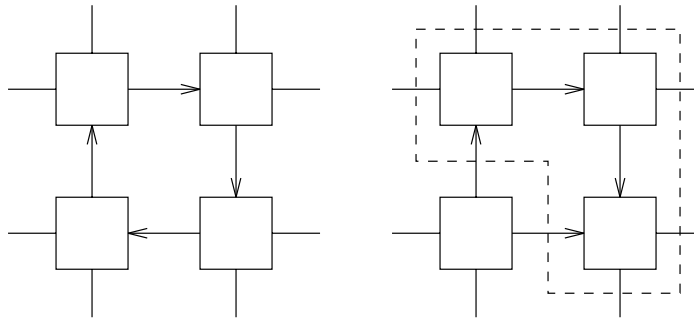


Figure 5: The two kinds of cycle

**Proof** We will use the ready specification formulation of deadlock-free types. Without loss of generality, assume that  $J = \{1, \dots, m\}$ . We need to show that

$$\text{readies}(P) \subseteq \{(s, X_1 \times \dots \times X_n) \mid \forall i. (\pi_i^*(s), X_i) \in \theta_i^\perp\}^\perp$$

i.e. that

$$\text{readies}(P) \perp \{(s, X_1 \times \dots \times X_n) \mid \forall i. (\pi_i^*(s), X_i) \in \theta_i^\perp\}.$$

Pick  $(s_1, X_1) \dots (s_n, X_n)$  and  $X$  such that for each  $i \in \{1, \dots, n\}$ ,  $(s_i, X_i) \in \theta_i^\perp$ , and  $(s, X) \in \text{readies}(P)$ , where  $s = s_1 \text{ zip } \dots \text{ zip } s_n$ . We need to show that  $(X_1 \times \dots \times X_n) \cap X \neq \emptyset$ .

For each  $i \in \{1, \dots, m\}$  pick  $a_i \in X_i$ . Because  $(s, X) \in \text{readies}(P)$  there is a process  $Q$  such that  $P \xrightarrow{s}^* Q$  and  $X = \text{initials}(Q)$ . Because  $P$  is receptive in ports  $\{1, \dots, m\}$ , for each  $j \in \{m+1, \dots, n\}$  there is  $a_j \in \Sigma_{A_j}(s_j)$  such that  $Q \xrightarrow{(a_1, \dots, a_n)} R$  for some  $R$ . Hence  $(a_1, \dots, a_n) \in X$ .

For each  $j \in \{m+1, \dots, n\}$  we have  $\theta_j^\perp = \text{in}$ , so  $X_j = \Sigma_{A_j}(s_j)$ , and  $a_j \in X_j$ . Hence  $(a_1, \dots, a_n) \in X_1 \times \dots \times X_n$ .  $\square$

This result allows any node to be assigned a type on the basis of a classification of its ports as inputs or outputs. If a network is constructed in  $\mathcal{S}Proc_D$  according to the type discipline, this corresponds to obeying the constraint that every connection is between an output and an input. As we know, the result is a network which will not deadlock. The type system of  $\mathcal{S}Proc_D$  does not allow cyclic connections to be established; however, cycles are very likely to be present in any interesting network, and we need to be able to construct them.

Now that we have identified certain ports as inputs, it is possible to see that not all cycles have the same structure. In Figure 5 the arrows point from outputs to inputs. Each of the two networks contains a cycle, but the patterns of flow of data are different. In the cycle on the right, one node has two outputs coming from it; if the part of the network enclosed in dashed lines is considered as a single node, this means that the cycle can be constructed by simultaneously connecting two outputs from one node to two inputs of another. The cycle on the left does not have this property, and represents a genuine feedback loop. In general, consider a polygon with  $n$  sides and orient each side by adding an arrow in one direction or the other. Starting from any vertex, follow the arrows; either we return to the initial vertex, or we arrive at a vertex with two arrows pointing at it. The first case corresponds to a feedback loop; in the second case, a dual argument shows that there is also a vertex with two arrows pointing away from it.

This means that any cycle which is not a feedback loop can be reduced to a simultaneous connection of two outputs from one process to two inputs of another, as in Figure 6. In

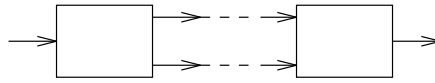


Figure 6: A double connection between nodes

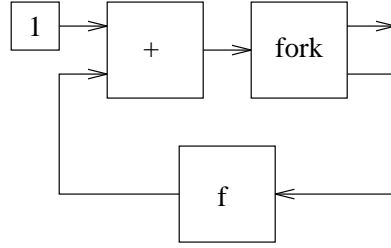


Figure 7: A network with feedback

$SProc_D$  we have processes  $P : (A, \text{in}_A) \wp (B, \text{out}_B) \wp (C, \text{out}_C)$  and  $Q : (B, \text{in}_B) \wp (C, \text{in}_C) \wp (D, \text{out}_D)$ . Writing  $P$  and  $Q$  as morphisms gives

$$\begin{aligned} P & : (A, \text{out}_A) \rightarrow (B, \text{out}_B) \wp (C, \text{out}_C) \\ Q & : (B, \text{out}_B) \otimes (C, \text{out}_C) \rightarrow (D, \text{out}_D) \end{aligned}$$

or equivalently

$$\begin{aligned} P & : (A, \text{out}_A) \rightarrow (B \wp C, \text{out}_B \wp \text{out}_C) \\ Q & : (B \otimes C, \text{out}_B \otimes \text{out}_C) \rightarrow (D, \text{out}_D). \end{aligned}$$

By Proposition 5.22 we have  $\text{out}_B \wp \text{out}_C = \text{out}_{B \wp C}$  and  $\text{out}_B \otimes \text{out}_C = \text{out}_{B \otimes C}$ . Combined with the fact that  $B \otimes C = B \wp C$ , this means that  $P$  and  $Q$  are composable, and we obtain  $P ; Q : (A, \text{out}_A) \rightarrow (D, \text{out}_D)$ , or equivalently  $P ; Q : (A, \text{in}_A) \wp (D, \text{out}_D)$ . Hence  $P ; Q$  is a deadlock-free process.

We now have to deal with the case of a feedback loop. As an example of the use of feedback in dataflow programming, consider the network in Figure 7. The node 1 produces the sequence  $111\dots$  and the function  $f$  is defined on streams by  $f(\sigma) = 0\sigma$ . The **fork** node simply copies its input, and the **+** node outputs at each instant the sum of the inputs received at the same instant. The output  $x$  is defined by  $x = 111\dots + f(x)$ , and the least solution of this equation (i.e. the least fixed point of  $\lambda x.111\dots + 0x$ ) is  $x = 1234\dots$ . The significant feature of  $f$  is that its first output token is independent of any input, and subsequently there is always a delay of one time unit between an input being received and the corresponding output being produced. For a dataflow network to be free of deadlock, every feedback loop should contain a node such as  $f$ . In LUSTRE, the corresponding node is called **pre**, and the language specifies that every loop must contain at least one **pre**. We will now give a semantic formulation of this property of nodes, and show that it yields a sufficient condition for the formation of deadlock-free cycles.

**Definition 7.3** Let  $P : (A_1, \text{in}) \wp \dots \wp (A_m, \text{in}) \wp (B_1, \text{out}) \wp \dots \wp (B_n, \text{out})$  in  $SProc_D$ . Output  $i$  of  $P$  is independent of input  $j$  if whenever  $P \xrightarrow{s} Q$ ,  $\forall a_1, \dots, a_{j-1}, a_{j+1}, \dots, a_m \exists b$  such that for all  $R$ ,  $Q \xrightarrow{(a_1, \dots, a_m, b_1, \dots, b_n)} R \Rightarrow b_i = b$ .

**Proposition 7.4** Suppose  $P : (A_1, \text{in}) \wp \dots \wp (A_m, \text{in}) \wp (A_{m+1}, \text{out}) \wp \dots \wp (A_n, \text{out}) \wp (A_{n+1}, \text{in}) \wp (A_{n+1}, \text{out})$  in  $SProc_D$  and let  $\bar{P}$  be the  $SProc$  process obtained by connecting ports  $(A_{n+1}, \text{out})$  and  $(A_{n+1}, \text{in})$  of  $P$ . If the output at port  $(A_{n+1}, \text{out})$  of  $P$  is independent of the input at port  $(A_{n+1}, \text{in})$ , then  $\bar{P} : (A_1, \text{in}) \wp \dots \wp (A_m, \text{in}) \wp (A_{m+1}, \text{out}) \wp \dots \wp (A_n, \text{out})$  in  $SProc_D$ .

**Proof** We need to show that

$$\text{readies}(P) \perp \{(s, X_1 \times \cdots \times X_n) \mid \forall i. (\pi_i^*(s), X_i) \in \theta_i^\perp\}$$

where  $\theta_1 \dots \theta_m$  are **in** and  $\theta_{m+1} \dots \theta_n$  are **out**.

Pick  $(s_1, X_1) \dots (s_n, X_n)$  and  $X$  such that for each  $i \in \{1, \dots, n\}$ ,  $(s_i, X_i) \in \theta_i^\perp$ , and  $(s, X) \in \text{readies}(\overline{P})$ , where  $s = s_1 \text{ zip } \dots \text{ zip } s_n$ . We need to show that  $(X_1 \times \cdots \times X_n) \cap X \neq \emptyset$ .

The definition of  $\overline{P}$  means that there is a trace  $t$  over  $A_{n+1}$  and a set  $Y$  such that  $(s \text{ zip } t \text{ zip } t, Y) \in \text{readies}(P)$ , and  $X = \{(x_1, \dots, x_n) \mid \exists y. (x_1, \dots, x_n, y, y) \in Y\}$ . Because the output at port  $(A_{n+1}, \text{out})$  of  $P$  is independent of the input at port  $(A_{n+1}, \text{in})$ , for any  $x_1, \dots, x_n$  there is  $b$  such that  $(x_1, \dots, x_n, y, z) \in Y \Rightarrow z = b$ .

Let  $X_{n+1} = \{b\}$  so that  $(t, X_{n+1}) \in \text{out}$ , and let  $X_{n+2} = \Sigma_{A_{n+1}}(t)$  so that  $(t, X_{n+2}) \in \text{in}$ . Because  $\text{readies}(P) \subseteq \text{in} \wp \cdots \wp \text{in} \wp \text{out} \wp \cdots \wp \text{out} \wp \text{in} \wp \text{out}$ , there is  $(a_1, \dots, a_n, y, z) \in (X_1 \times \cdots \times X_{n+2}) \cap Y$ .

We have  $z = b$ , dependent only on  $a_1, \dots, a_n$ . Because  $X_{n+1} = \{b\}$ ,  $y = b$ . So we have  $(a_1, \dots, a_n, b, b) \in Y$ , and hence  $(a_1, \dots, a_n) \in X$ . Therefore  $(X_1 \times \cdots \times X_n) \cap X \neq \emptyset$ , as required.  $\square$

We will use the term *source* to describe an output which is independent of any input which forms part of a cycle under consideration. In previous work [21] the term *source* has been used to describe an output which is independent of *all* inputs, but here we will use this weaker definition. The process  $P$  in Proposition 7.4 represents the network at the last stage of construction, just before formation of the cycle. In practice, and in line with the LUSTRE condition that every loop contains a **pre** node, we would like to deduce that the appropriate output of  $P$  is a source from the fact that one of the nodes used to construct  $P$  has a source. It can be shown, assuming that the outputs of nodes depend functionally on the inputs and that nodes are deterministic (these conditions are always satisfied for a language such as LUSTRE), that sources are preserved by composition [21]. Hence it is sufficient to check that there is a source somewhere in every cycle.

### 7.3 Generalisations

In our analysis of networks, we have simply identified each port as either an input or an output. However, we can imagine more general situations in which a particular port may behave in different ways at different times; for example, being receptive at the first step (and thus behaving as an input) but subsequently behaving as an output. In general, consider any finite sequence of **in** and **out** symbols, and interpret such a sequence as specifying the repeating unit of a communication pattern. For example, the sequence **in.out** represents an infinite alternation of input and output. The type system of  $\mathcal{S}Proc_D$  is rich enough to include semantic versions of such communication patterns over any  $\mathcal{S}Proc$  type. Continuing the example, the interpretation of the sequence **in.out** over the  $\mathcal{S}Proc$  type  $A$  would be the ready specification

$$\{(s, \Sigma_A(s)) \mid \text{length}(s) \text{ is even}\} \cup \{(s, X) \mid X \subseteq \Sigma_A(s), \text{length}(s) \text{ is odd}\}.$$

A detailed development of this idea, which is a subject for future work, should lead to interesting connections with the type system proposed by Takeuchi *et al.* [46].

## 8 Conclusions

We have proposed a type-theoretic view of the specification and verification of concurrent systems. The relevant technical machinery is the notion of *specification structures*, which provides a systematic approach to the construction of a hierarchy of type systems expressing increasingly strong specifications. We have illustrated this idea by defining a specification structure over  $\mathcal{S}Proc$ , a category of synchronous processes. The resulting category,  $\mathcal{S}Proc_D$ , has a type system which permits compositional verification of deadlock-freedom. We have presented two equivalent definitions of  $\mathcal{S}Proc_D$ , one based on the idea of a type as a set of processes, the other based on the notion of ready specification. As a simple application, we have shown that  $\mathcal{S}Proc_D$  supports the types necessary to specify and verify deadlock-freedom of synchronous networks; examples of synchronous networks include synchronous dataflow programs and systolic algorithms.

A number of type systems for concurrency have been proposed recently. Many of them are based on the idea of identifying ports or channels as input or outputs, and checking that outputs are always connected to inputs. There are several variations which include information about how many times channels are used [31], the order of usage of channels [30], subtyping [43], types for choice and branching behaviour [46]. The distinguishing features of our approach are as follows. First, it is based on interaction categories, a theory which emphasises the collective structure of processes and describes this structure using the language of category theory. Second, we have proposed a methodology (via the notion of a specification structure) for treating a range of program properties within a single framework, and combining type-checking with other verification techniques. Finally, we have taken a more semantic view of our type system. In our examples, the types assigned to individual ports correspond to either inputs or outputs, but semantically an arbitrary combination of such types can be treated on the same footing as any other type. This means that our arguments for correctness of networks, although intuitively based on considerations of input vs. output and information flow, are formalised within a uniform semantic setting.

There are many ways in which the theory described in this paper could be extended and developed. Progress has already been made on an asynchronous version of the theory, by applying the sets of processes approach to the asynchronous interaction category  $\mathcal{AS}Proc$  [6]. The result is a category of deadlock-free processes in which the global synchrony condition is not present. Preliminary versions of this work have appeared in [2, 21] and an improved version in [40]; a full report of this area will be the subject of a future paper. Another avenue of investigation is the development of a formal syntax, incorporating value-passing rather than simply synchronisation, with which to describe processes in  $\mathcal{S}Proc_D$ . We have also mentioned, in Section 7, the possibility of giving a semantics to general communication patterns similar to those in the type system of Takeuchi *et al.* [46].

There are two major respects in which our deadlock-free type system is perhaps a little too restrictive. First, we have not yet addressed the issue of mobility [37, 38], which has featured prominently in recent research on concurrency theory. Second, the property guaranteed by type system is extremely strong—all processes must run forever. This is the reason why, in our applications, extra analysis is needed in order to construct cyclic networks. Most other proposed type systems for concurrency use types to guarantee slightly weaker properties—for example, that any communication which occurs must be correct, but not that communication must always continue. This problem is alleviated slightly by the asynchronous version of our theory, which incorporates a notion of successful termination, but we would like to find a modification of the theory which would make the type system weaker but correspondingly more flexible. Static analysis techniques, as well as type-checking techniques, may then be appropriate for establishing program properties.



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